

ADwin - connector pin assignments

last update (yyyy-mm-dd): 2006-09-06

On the following pages you will find the connector-pinouts for all
ADwin-cards, **ADwin-Pro**-, **ADwin-Gold**- and **ADwin-light16**-systems.

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ADwin - analog-/digital-cards

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<p>DAC 5 OUT DAC 6 OUT ADC 7 IN (ADC0 Ch4) ADC 9 IN (ADC0 Ch5) ADC 11 IN (ADC0 Ch6) ADC 13 IN (ADC0 Ch7) ADC 8 IN (ADC1 Ch4) ADC 10 IN (ADC1 Ch5) ADC 12 IN (ADC1 Ch6) ADC 15 IN (ADC0 Ch8) ADC 14 IN (ADC1 Ch7) ADC 16 IN (ADC1 Ch8) BIT 9 OUT BIT 10 OUT BIT 11 OUT BIT 12 OUT BIT 13 OUT BIT 14 OUT BIT 15 OUT</p> <p>DIG GND AGND ADC BIT 9 IN BIT 10 IN BIT 11 IN BIT 12 IN BIT 13 IN BIT 14 IN BIT 15 IN</p>	<p>DAC 5 OUT DAC 6 OUT ADC 7 IN (ADC0 Ch4) ADC 9 IN (ADC0 Ch5) ADC 11 IN (ADC0 Ch6) ADC 13 IN (ADC0 Ch7) ADC 8 IN (ADC1 Ch4) ADC 10 IN (ADC1 Ch5) ADC 12 IN (ADC1 Ch6) BIT 6 OUT BIT 7 OUT BIT 8 OUT BIT 9 OUT BIT 10 OUT BIT 11 OUT BIT 12 OUT BIT 13 OUT BIT 14 OUT BIT 15 OUT</p> <p>DIG GND AGND ADC BIT 6 IN BIT 7 IN BIT 8 IN BIT 9 IN BIT 10 IN BIT 11 IN BIT 12 IN BIT 13 IN BIT 14 IN BIT 15 IN</p>	<p>DAC 1 OUT DAC 2 OUT DAC 3 OUT DAC 4 OUT ADC 1 IN (ADC0 Ch1) ADC 3 IN (ADC0 Ch2) ADC 5 IN (ADC0 Ch3) ADC 2 IN (ADC1 Ch1) ADC 4 IN (ADC1 Ch2) ADC 6 IN (ADC1 Ch3) BIT 0 IN BIT 1 IN BIT 2 IN BIT 3 IN BIT 4 IN BIT 5 IN DIG GND +12V (100mA max.) +5V (100mA max.)</p> <p>AGND DAC AGND ADC BIT 0 OUT BIT 1 OUT BIT 2 OUT BIT 3 OUT BIT 4 OUT BIT 5 OUT EVENT IN -12V (100mA max.) -5V (100mA max.)</p>
<p>ADwin add-on connector (16 AIN / 26 DIO)</p>	<p>ADwin add-on connector (13 AIN / 32 DIO)</p>	<p>ADwin</p>
<p>DAC 1 OUT DAC 2 OUT ADC 13 IN (+) ADC 15 IN (+) ADC 1 IN (+) ADC 3 IN (+) ADC 5 IN (+) ADC 7 IN (+) ADC 9 IN (+) ADC 11 IN (+) BIT 0 IN BIT 1 IN BIT 2 IN BIT 3 IN CNTR A / BIT 4 IN CNTR B / BIT 5 IN DIG GND +12V (100mA max.) +5V (100mA max.)</p> <p>AGND DAC ADC 13 IN (-) ADC 15 IN (-) ADC 1 IN (-) ADC 3 IN (-) ADC 5 IN (-) ADC 7 IN (-) ADC 9 IN (-) ADC 11 IN (-) BIT 0 OUT BIT 1 OUT BIT 2 OUT BIT 3 OUT BIT 4 OUT BIT 5 OUT EVENT IN -12V (100mA max.) -5V (100mA max.)</p>	<p>DAC 3 OUT DAC 4 OUT ADC 14 IN + (ADC1 Ch7) ADC 16 IN + (ADC1 Ch8) ADC 2 IN + (ADC1 Ch1) ADC 4 IN + (ADC1 Ch2) ADC 6 IN + (ADC1 Ch3) ADC 8 IN + (ADC1 Ch4) ADC 10 IN + (ADC1 Ch5) ADC 12 IN + (ADC1 Ch6) BIT 6 IN BIT 7 IN BIT 8 IN BIT 9 IN BIT 10 IN BIT 11 IN DIG GND +12V (100mA max.) +5V (100mA max.)</p> <p>AGND DAC ADC 14 IN - (ADC1 Ch7) ADC 16 IN - (ADC1 Ch8) ADC 2 IN - (ADC1 Ch1) ADC 4 IN - (ADC1 Ch2) ADC 6 IN - (ADC1 Ch3) ADC 8 IN - (ADC1 Ch4) ADC 10 IN - (ADC1 Ch5) ADC 12 IN - (ADC1 Ch6) BIT 6 OUT BIT 7 OUT BIT 8 OUT BIT 9 OUT BIT 10 OUT BIT 11 OUT EVENT IN -12V (100mA max.) -5V (100mA max.)</p>	<p>DAC 1 OUT DAC 2 OUT ADC 13 IN + (ADC0 Ch7) ADC 15 IN + (ADC0 Ch8) ADC 1 IN + (ADC0 Ch1) ADC 3 IN + (ADC0 Ch2) ADC 5 IN + (ADC0 Ch3) ADC 7 IN + (ADC0 Ch4) ADC 9 IN + (ADC0 Ch5) ADC 11 IN + (ADC0 Ch6) BIT 0 IN BIT 1 IN BIT 2 IN BIT 3 IN CNTR 1 / BIT 4 IN CNTR 2 / BIT 5 IN DIG GND +12V (100mA max.) +5V (100mA max.)</p> <p>AGND DAC ADC 13 IN - (ADC0 Ch7) ADC 15 IN - (ADC0 Ch8) ADC 1 IN - (ADC0 Ch1) ADC 3 IN - (ADC0 Ch2) ADC 5 IN - (ADC0 Ch3) ADC 7 IN - (ADC0 Ch4) ADC 9 IN - (ADC0 Ch5) ADC 11 IN - (ADC0 Ch6) BIT 0 OUT BIT 1 OUT BIT 2 OUT BIT 3 OUT BIT 4 OUT BIT 5 OUT EVENT IN -12V (100mA max.) -5V (100mA max.)</p>
<p>ADwin-CO1L</p>	<p>ADwin-ADL</p>	<p>ADwin-light</p>
		<p>DIO 00 DIO 03 DIO 06 DIO 09 DIO 12 DIO 15 DIO 36 DIO 39 DIO 18 DIO 21 DIO 24 DIO 27 DIO 30 DIO 35 / Co4 DGND DGND</p> <p>DIO 01 DIO 04 DIO 07 DIO 10 DIO 13 DIO 32 / Co1 DIO 37 DIO 16 DIO 19 DIO 22 DIO 25 DIO 28 DIO 31 DIO 40 DIO 43 DGND</p> <p>DIO 02 DIO 05 DIO 08 DIO 11 DIO 14 DIO 33 / Co2 DIO 38 DIO 17 DIO 20 DIO 23 DIO 26 DIO 29 DIO 34 / Co3 DIO 41 DGND EVENT IN +5V(100mA)</p>
		<p>PWM-5 OUTPUT RESERVED PWM-1 OUTPUT RESERVED PWM-4 OUTPUT RESERVED GND GND +12V (max. 100mA) +5V (max. 100mA)</p> <p>RESERVED PWM-3 OUTPUT RESERVED PWM-2 OUTPUT RESERVED PWM-6 OUTPUT RESERVED EVENT IN -12V (max. 100mA) -5V (max. 100mA)</p>
		<p>ADwin-PWM6</p>

If not otherwise noted, all connectors are of female type.

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ADwin - counter-cards

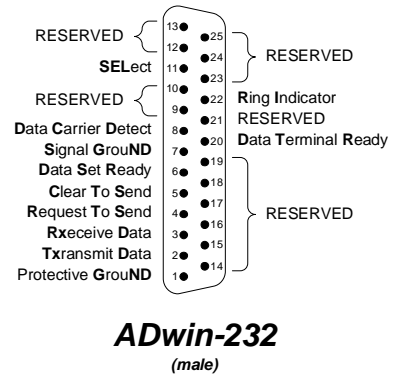
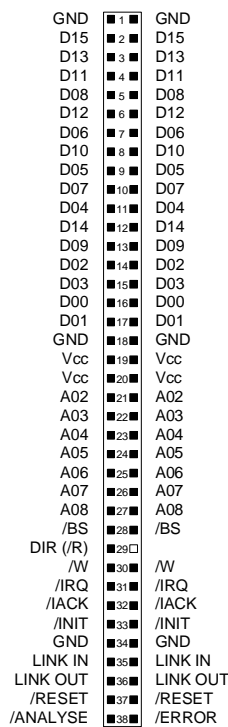
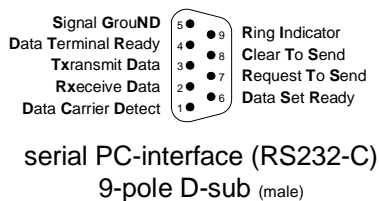
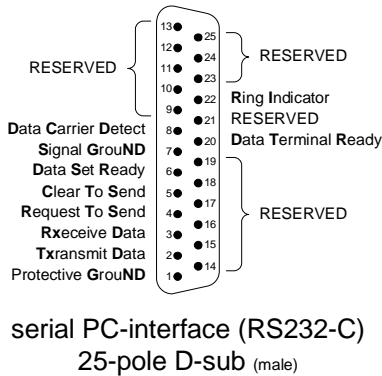
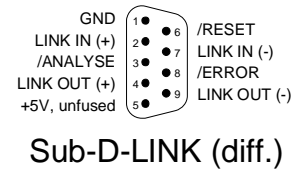
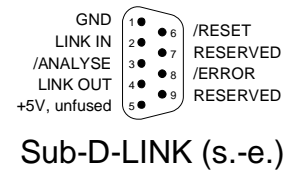
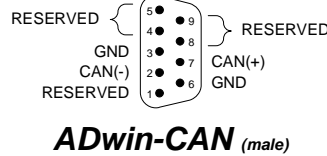
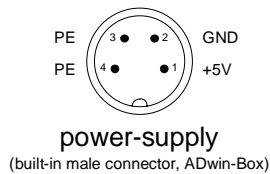
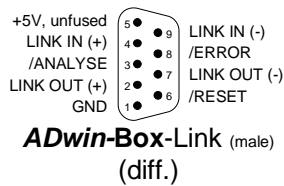
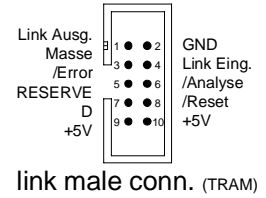
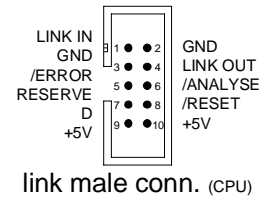
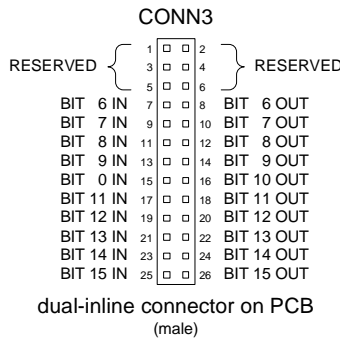
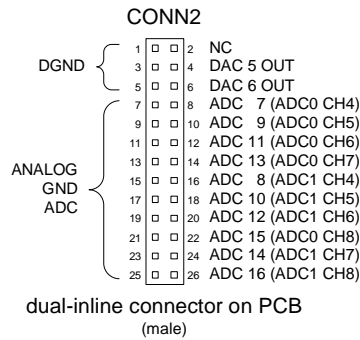
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<p>ADwin-CO6</p>	<p>ADwin-CO12</p>	<p>ADwin-CO24</p>
		<p>ADwin-CO4VR4</p>
		<p>ADwin-CO8VR2</p>
<p>ADwin-VR6-L</p>	<p>ADwin-VR6</p>	

If not otherwise noted, all connectors are of female type.

ADwin - various connectors

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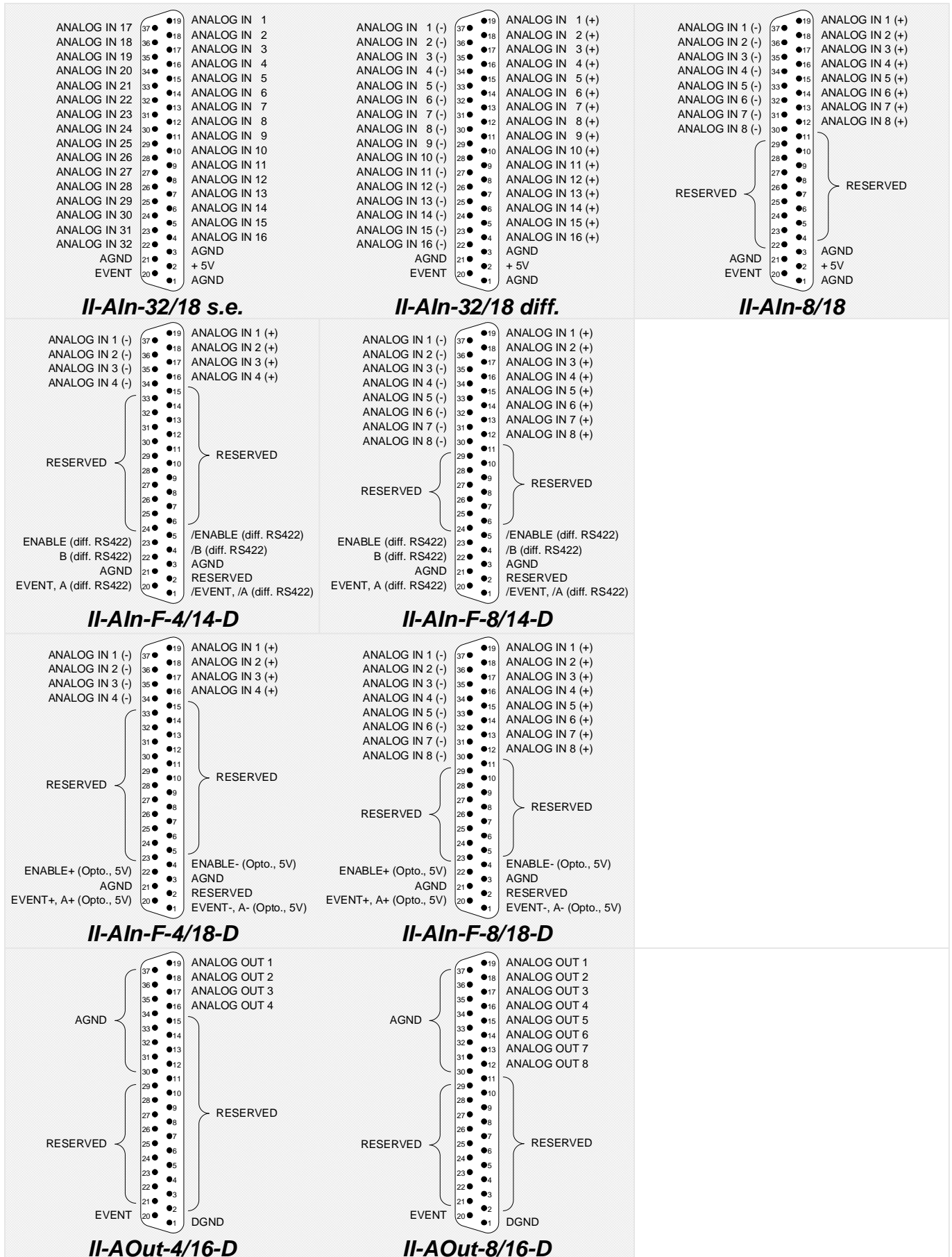
If not otherwise noted, all connectors are of female type.

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ADwin-Pro-II - ADC/DAC-modules

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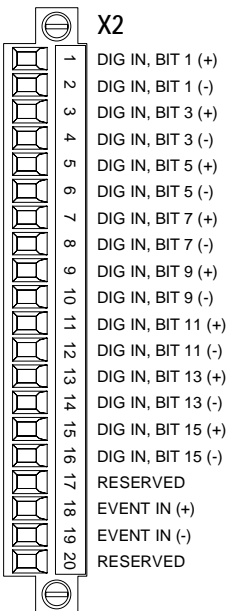
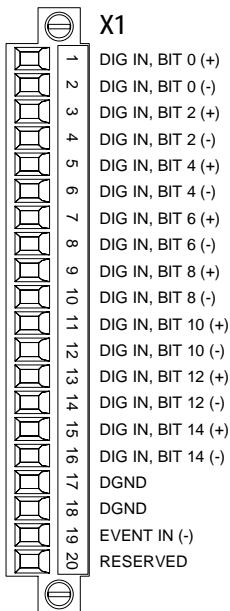
If not otherwise noted, all connectors are of female type.

ADwin-Pro - DIO-modules (part 1)

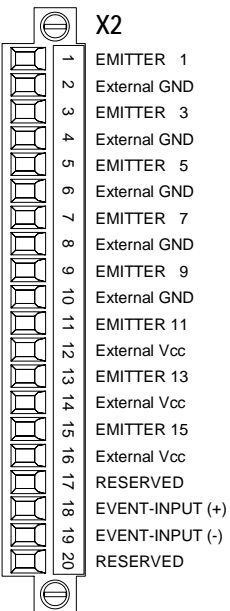
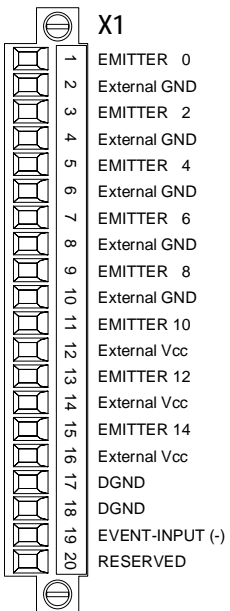
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<p>DIG I/O, BIT 1 37 ● 19 DIG I/O, BIT 3 36 ● 18 DIG I/O, BIT 5 35 ● 17 DIG I/O, BIT 7 34 ● 16 DIG I/O, BIT 9 33 ● 15 DIG I/O, BIT 11 32 ● 14 DIG I/O, BIT 13 31 ● 13 DIG I/O, BIT 15 30 ● 12 DIG I/O, BIT 17 29 ● 11 DIG I/O, BIT 19 28 ● 10 DIG I/O, BIT 21 27 ● 9 DIG I/O, BIT 23 26 ● 8 DIG I/O, BIT 25 25 ● 7 DIG I/O, BIT 27 24 ● 6 DIG I/O, BIT 29 23 ● 5 DIG I/O, BIT 31 22 ● 4 DGND 21 ● 3 EVENT IN 20 ● 2 1 ● 1</p> <p>DIG I/O, BIT 0 37 ● 19 DIG I/O, BIT 2 36 ● 18 DIG I/O, BIT 4 35 ● 17 DIG I/O, BIT 6 34 ● 16 DIG I/O, BIT 8 33 ● 15 DIG I/O, BIT 10 32 ● 14 DIG I/O, BIT 12 31 ● 13 DIG I/O, BIT 14 30 ● 12 DIG I/O, BIT 16 29 ● 11 DIG I/O, BIT 18 28 ● 10 DIG I/O, BIT 20 27 ● 9 DIG I/O, BIT 22 26 ● 8 DIG I/O, BIT 24 25 ● 7 DIG I/O, BIT 26 24 ● 6 DIG I/O, BIT 28 23 ● 5 DIG I/O, BIT 30 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 DGND 1 ● 1</p> <p>DIO-32</p>	<p>RELAY 0 A 37 ● 19 RELAY 1 A 36 ● 18 RELAY 2 A 35 ● 17 RELAY 3 A 34 ● 16 RELAY 4 A 33 ● 15 RELAY 5 A 32 ● 14 RELAY 6 A 31 ● 13 RELAY 7 A 30 ● 12 RELAY 8 A 29 ● 11 RELAY 9 A 28 ● 10 RELAY 10 A 27 ● 9 RELAY 11 A 26 ● 8 RELAY 12 A 25 ● 7 RELAY 13 A 24 ● 6 RELAY 14 A 23 ● 5 RELAY 15 A 22 ● 4 DGND 21 ● 3 EVENT IN (+) 20 ● 2 1 ● 1</p> <p>RELAY 0 B 37 ● 19 RELAY 1 B 36 ● 18 RELAY 2 B 35 ● 17 RELAY 3 B 34 ● 16 RELAY 4 B 33 ● 15 RELAY 5 B 32 ● 14 RELAY 6 B 31 ● 13 RELAY 7 B 30 ● 12 RELAY 8 B 29 ● 11 RELAY 9 B 28 ● 10 RELAY 10 B 27 ● 9 RELAY 11 B 26 ● 8 RELAY 12 B 25 ● 7 RELAY 13 B 24 ● 6 RELAY 14 B 23 ● 5 RELAY 15 B 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 EVENT IN (-) 1 ● 1</p> <p>REL-16</p>	<p>External GND 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>External Vcc 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>DGND 21 ● 3 EVENT IN (+) 20 ● 2 1 ● 1</p> <p>EMITTER 0 37 ● 19 EMITTER 1 36 ● 18 EMITTER 2 35 ● 17 EMITTER 3 34 ● 16 EMITTER 4 33 ● 15 EMITTER 5 32 ● 14 EMITTER 6 31 ● 13 EMITTER 7 30 ● 12 EMITTER 8 29 ● 11 EMITTER 9 28 ● 10 EMITTER 10 27 ● 9 EMITTER 11 26 ● 8 EMITTER 12 25 ● 7 EMITTER 13 24 ● 6 EMITTER 14 23 ● 5 EMITTER 15 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 EVENT IN (-) 1 ● 1</p> <p>TRA-16</p>
<p>DIG I/O, BIT 1 37 ● 19 DIG I/O, BIT 3 36 ● 18 DIG I/O, BIT 5 35 ● 17 DIG I/O, BIT 7 34 ● 16 DIG I/O, BIT 9 33 ● 15 DIG I/O, BIT 11 32 ● 14 DIG I/O, BIT 13 31 ● 13 DIG I/O, BIT 15 30 ● 12 DIG I/O, BIT 17 29 ● 11 DIG I/O, BIT 19 28 ● 10 DIG I/O, BIT 21 27 ● 9 DIG I/O, BIT 23 26 ● 8 DIG I/O, BIT 25 25 ● 7 DIG I/O, BIT 27 24 ● 6 DIG I/O, BIT 29 23 ● 5 DIG I/O, BIT 31 22 ● 4 DGND 21 ● 3 EVENT-INPUT 20 ● 2 1 ● 1</p> <p>DIG I/O, BIT 0 37 ● 19 DIG I/O, BIT 2 36 ● 18 DIG I/O, BIT 4 35 ● 17 DIG I/O, BIT 6 34 ● 16 DIG I/O, BIT 8 33 ● 15 DIG I/O, BIT 10 32 ● 14 DIG I/O, BIT 12 31 ● 13 DIG I/O, BIT 14 30 ● 12 DIG I/O, BIT 16 29 ● 11 DIG I/O, BIT 18 28 ● 10 DIG I/O, BIT 20 27 ● 9 DIG I/O, BIT 22 26 ● 8 DIG I/O, BIT 24 25 ● 7 DIG I/O, BIT 26 24 ● 6 DIG I/O, BIT 28 23 ● 5 DIG I/O, BIT 30 22 ● 4 DGND 21 ● 3 +5V, <100mA (fused) 20 ● 2 DGND 1 ● 1</p> <p>DIO-32-RB</p>	<p>External GND 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>External Vcc 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>RESERVED 21 ● 3 EVENT IN (+) 20 ● 2 1 ● 1</p> <p>PWM OUTPUT 1 (+) 37 ● 19 RESERVED 36 ● 18 RESERVED 35 ● 17 PWM OUTPUT 2 (+) 34 ● 16 RESERVED 33 ● 15 RESERVED 32 ● 14 PWM OUTPUT 3 (+) 31 ● 13 RESERVED 30 ● 12 RESERVED 29 ● 11 PWM OUTPUT 4 (+) 28 ● 10 RESERVED 27 ● 9 RESERVED 26 ● 8 RESERVED 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 RESERVED 22 ● 4 RESERVED 21 ● 3 EVENT IN (-) 20 ● 2 1 ● 1</p> <p>PWM-4-I</p>	<p>RESERVED 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>RESERVED 37 ● 19 PWM OUTPUT 1 36 ● 18 RESERVED 35 ● 17 RESERVED 34 ● 16 PWM OUTPUT 2 33 ● 15 RESERVED 32 ● 14 RESERVED 31 ● 13 PWM OUTPUT 3 30 ● 12 RESERVED 29 ● 11 PWM OUTPUT 4 28 ● 10 RESERVED 27 ● 9 RESERVED 26 ● 8 PWM OUTPUT 4 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 RESERVED 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 DGND 1 ● 1</p> <p>DGND 21 ● 3 EVENT IN 20 ● 2 1 ● 1</p> <p>PWM-4</p>
<p>DGND 37 ● 19 36 ● 18 35 ● 17 34 ● 16 33 ● 15 32 ● 14 31 ● 13 30 ● 12 29 ● 11 28 ● 10 27 ● 9 26 ● 8 25 ● 7 24 ● 6 23 ● 5 22 ● 4 21 ● 3 20 ● 2 1 ● 1</p> <p>INPUT 0 37 ● 19 INPUT 1 36 ● 18 INPUT 2 35 ● 17 INPUT 3 34 ● 16 INPUT 4 33 ● 15 INPUT 5 32 ● 14 INPUT 6 31 ● 13 INPUT 7 30 ● 12 INPUT 8 29 ● 11 INPUT 9 28 ● 10 INPUT 10 27 ● 9 INPUT 11 26 ● 8 INPUT 12 25 ● 7 INPUT 13 24 ● 6 INPUT 14 23 ● 5 INPUT 15 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 DGND 1 ● 1</p> <p>COMP-16</p>		<p>DIG IN, BIT 0 (-) 37 ● 19 DIG IN, BIT 1 (-) 36 ● 18 DIG IN, BIT 2 (-) 35 ● 17 DIG IN, BIT 3 (-) 34 ● 16 DIG IN, BIT 4 (-) 33 ● 15 DIG IN, BIT 5 (-) 32 ● 14 DIG IN, BIT 6 (-) 31 ● 13 DIG IN, BIT 7 (-) 30 ● 12 DIG IN, BIT 8 (-) 29 ● 11 DIG IN, BIT 9 (-) 28 ● 10 DIG IN, BIT 10 (-) 27 ● 9 DIG IN, BIT 11 (-) 26 ● 8 DIG IN, BIT 12 (-) 25 ● 7 DIG IN, BIT 13 (-) 24 ● 6 DIG IN, BIT 14 (-) 23 ● 5 DIG IN, BIT 15 (-) 22 ● 4 DGND 21 ● 3 EVENT IN (+) 20 ● 2 1 ● 1</p> <p>DIG IN, BIT 0 (+) 37 ● 19 DIG IN, BIT 1 (+) 36 ● 18 DIG IN, BIT 2 (+) 35 ● 17 DIG IN, BIT 3 (+) 34 ● 16 DIG IN, BIT 4 (+) 33 ● 15 DIG IN, BIT 5 (+) 32 ● 14 DIG IN, BIT 6 (+) 31 ● 13 DIG IN, BIT 7 (+) 30 ● 12 DIG IN, BIT 8 (+) 29 ● 11 DIG IN, BIT 9 (+) 28 ● 10 DIG IN, BIT 10 (+) 27 ● 9 DIG IN, BIT 11 (+) 26 ● 8 DIG IN, BIT 12 (+) 25 ● 7 DIG IN, BIT 13 (+) 24 ● 6 DIG IN, BIT 14 (+) 23 ● 5 DIG IN, BIT 15 (+) 22 ● 4 DGND 21 ● 3 RESERVED 20 ● 2 EVENT IN (-) 1 ● 1</p> <p>OPT-16</p>

If not otherwise noted, all connectors are of female type.



OPT-16



TRA-16

If not otherwise noted, all connectors are of female type.

ADwin-Pro - counter-modules (part 1)

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ADwin-Pro - counter-modules (part 2)

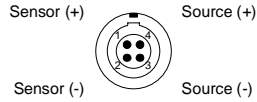
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	<div><div><div>RESERVED</div><div>CNTR 1 (A / CLK) (-)</div><div>CNTR 1 (B / DIR) (-)</div><div>CNTR 1 CLR(-)</div><div>RESERVED</div><div>CNTR 2 (A / CLK) (-)</div><div>CNTR 2 (B / DIR) (-)</div><div>CNTR 2 CLR(-)</div><div>PW-INPUT 3 (-)</div><div>RESERVED</div><div>PW-INPUT 4 (-)</div><div>RESERVED</div><div>EVENT-IN (+)</div></div><div><div>●19</div><div>●18</div><div>●17</div><div>●16</div><div>●15</div><div>●14</div><div>●13</div><div>●12</div><div>●11</div><div>●10</div><div>●9</div><div>●8</div><div>●7</div><div>●6</div><div>●5</div><div>●4</div><div>●3</div><div>●2</div><div>●1</div></div></div> <div>CNT-VR2PW2-I</div>	<div><div><div>CNTR 1 DIR</div><div>CNTR 1 B</div><div>RESERVED</div><div>CNTR 2 DIR</div><div>CNTR 2 B</div><div>RESERVED</div><div>CNTR 3 DIR</div><div>CNTR 3 B</div><div>RESERVED</div><div>CNTR 4 DIR</div><div>CNTR 4 B</div><div>RESERVED</div><div>DGND</div><div>EVENT-IN</div></div><div><div>●19</div><div>●18</div><div>●17</div><div>●16</div><div>●15</div><div>●14</div><div>●13</div><div>●12</div><div>●11</div><div>●10</div><div>●9</div><div>●8</div><div>●7</div><div>●6</div><div>●5</div><div>●4</div><div>●3</div><div>●2</div><div>●1</div></div></div> <div>CNT-VR2PW2</div>
<div><div><div>CNTR 1 DIR</div><div>CNTR 1 B</div><div>RESERVED</div><div>CNTR 2 DIR</div><div>CNTR 2 B</div><div>RESERVED</div><div>CNTR 3 DIR</div><div>CNTR 3 B</div><div>RESERVED</div><div>CNTR 4 DIR</div><div>CNTR 4 B</div><div>RESERVED</div><div>DGND</div><div>EVENT-IN</div></div><div><div>●19</div><div>●18</div><div>●17</div><div>●16</div><div>●15</div><div>●14</div><div>●13</div><div>●12</div><div>●11</div><div>●10</div><div>●9</div><div>●8</div><div>●7</div><div>●6</div><div>●5</div><div>●4</div><div>●3</div><div>●2</div><div>●1</div></div></div> <div>CO4-T</div>	<div><div><div>SSI 1, CLK (-)</div><div>CNTR 1, A/CLK/PWM (-)</div><div>CNTR 1, B/DIR (-)</div><div>CNTR 1, CLR/LATCH (-)</div><div>SSI 1, DATA (-)</div><div>CNTR 2, A/CLK/PWM (-)</div><div>CNTR 2, B/DIR (-)</div><div>CNTR 2, CLR/LATCH (-)</div><div>SSI 2, CLK (-)</div><div>CNTR 3, A/CLK/PWM (-)</div><div>CNTR 3, B/DIR (-)</div><div>CNTR 3, CLR/LATCH (-)</div><div>SSI 2, DATA (-)</div><div>CNTR 4, A/CLK/PWM (-)</div><div>CNTR 4, B/DIR (-)</div><div>CNTR 4, CLR/LATCH (-)</div><div>DGND</div><div>EVENT-IN (+)</div></div><div><div>●19</div><div>●18</div><div>●17</div><div>●16</div><div>●15</div><div>●14</div><div>●13</div><div>●12</div><div>●11</div><div>●10</div><div>●9</div><div>●8</div><div>●7</div><div>●6</div><div>●5</div><div>●4</div><div>●3</div><div>●2</div><div>●1</div></div></div> <div>CO4-D</div>	<div><div><div>RESERVED</div><div>CNTR 1, A/CLK/PWM (-)</div><div>CNTR 1, B/DIR (-)</div><div>CNTR 1, CLR/LATCH (-)</div><div>RESERVED</div><div>CNTR 2, A/CLK/PWM (-)</div><div>CNTR 2, B/DIR (-)</div><div>CNTR 2, CLR/LATCH (-)</div><div>RESERVED</div><div>CNTR 3, A/CLK/PWM (-)</div><div>CNTR 3, B/DIR (-)</div><div>CNTR 3, CLR/LATCH (-)</div><div>RESERVED</div><div>CNTR 4, A/CLK/PWM (-)</div><div>CNTR 4, B/DIR (-)</div><div>CNTR 4, CLR/LATCH (-)</div><div>RESERVED</div><div>EVENT-IN (+)</div></div><div><div>●19</div><div>●18</div><div>●17</div><div>●16</div><div>●15</div><div>●14</div><div>●13</div><div>●12</div><div>●11</div><div>●10</div><div>●9</div><div>●8</div><div>●7</div><div>●6</div><div>●5</div><div>●4</div><div>●3</div><div>●2</div><div>●1</div></div></div> <div>CO4-I</div>

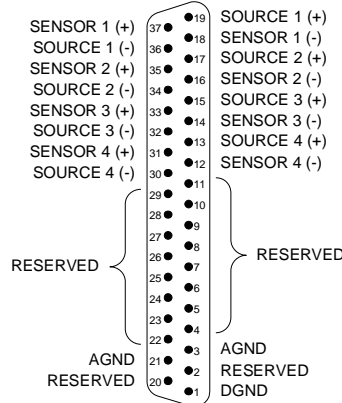
If not otherwise noted, all connectors are of female type.

ADwin-Pro - EXT-modules (part 1)

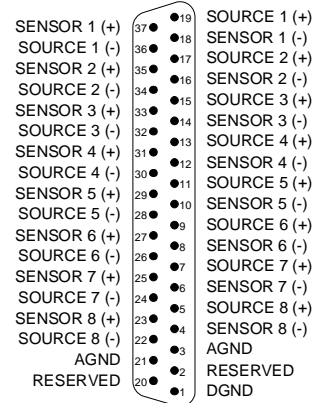
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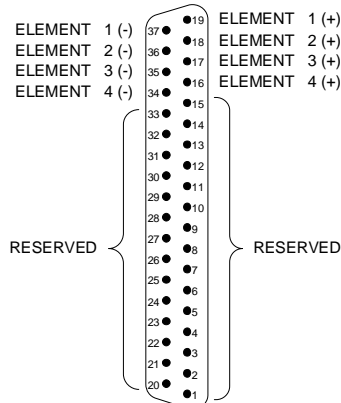
PT100



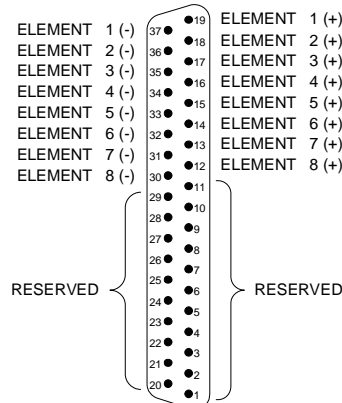
PT100-4-D



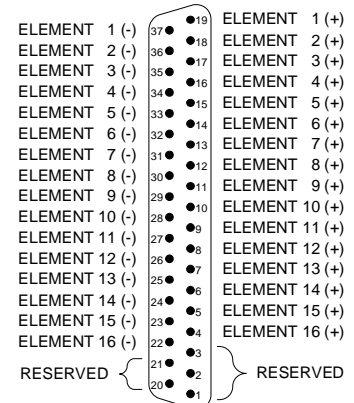
PT100-8-D



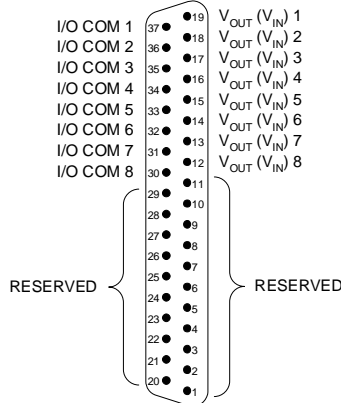
TC-4-J(-K)-D



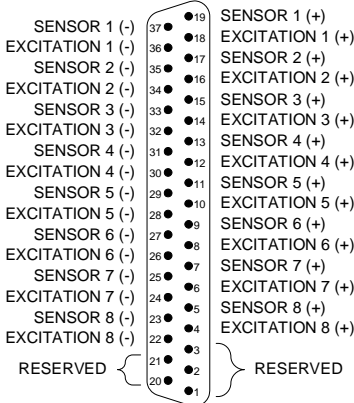
TC-8-J(-K)-D



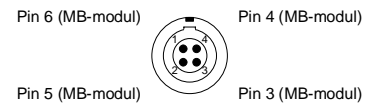
TC-16-J(-K)



MB-8 Module-Output



MB-8 Module-Input

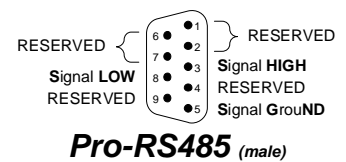
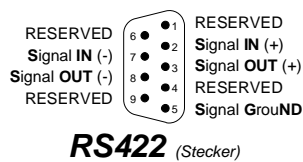
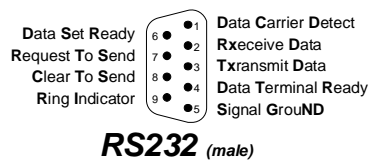
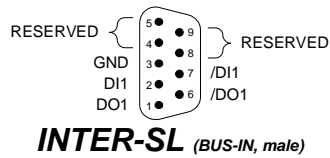
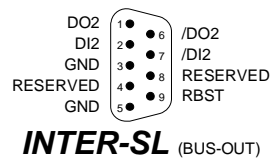
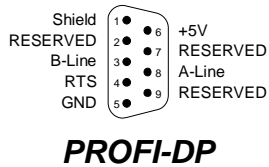
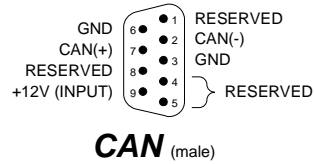
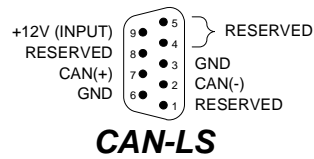
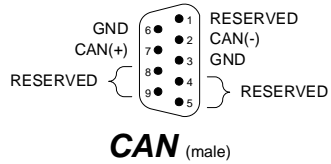
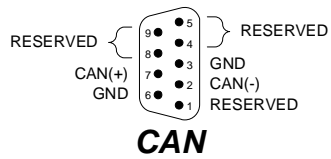


MB-8

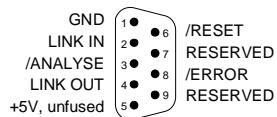
If not otherwise noted, all connectors are of female type.

ADwin-Pro - EXT-modules (part 2)

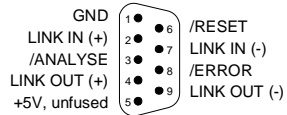
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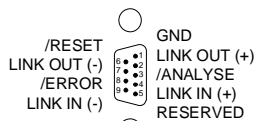
If not otherwise noted, all connectors are of female type.



D-sub-LINK (s.-e.)

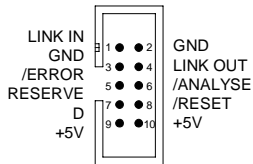


D-sub-LINK (diff.)



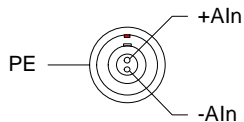
Link

(built-in male connector)

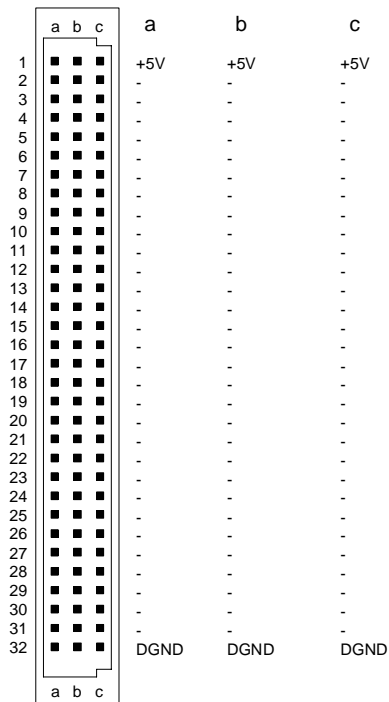


link-connector (CPU)

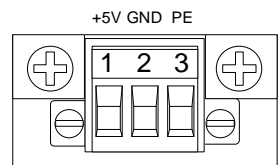
(male)



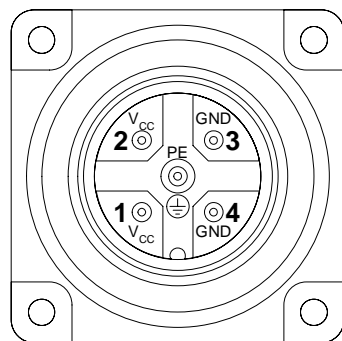
2-pole LEMO fem. connector



**ADwin-Pro, VG96
(Backplane)**



Pro-Mini (power-supply)

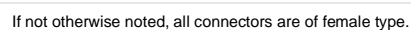


**Pro-DC (power-supply)
(male)**

If not otherwise noted, all connectors are of female type.

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ADwin-Gold-D (part 1)

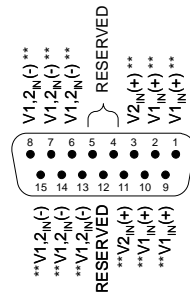
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<p>ANALOG OUT (male) <small>*with -DA extension</small></p>	<p>ANALOG IN (female)</p>	<p>ADwin-Gold- power-supply (male)</p>
<p>DIO 00-15 (IN) (male)</p> <p><small>**with -CO1 extension</small></p>	<p>DIO 00-15 (IN) (male)</p> <p>with CONF_DIO(12)</p>	
<p>DIO 16-31 (OUT)</p> <p><small>**with -CO1 extension</small></p>	<p>DIO 16-31 (OUT)</p> <p>with CONF_DIO(12)</p>	

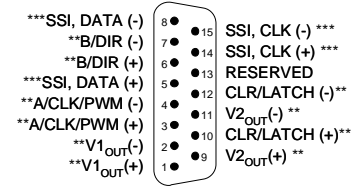
If not otherwise noted, all connectors are of female type.

ADwin-Gold-D (part 2)

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****CO POWER IN**



****CO1, ... , CO4 (male)**

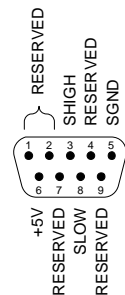
with -CO1 extension, *with COM extension

RS232 legend:

DCD - Data Carrier Detect
RxD - Receive Data
TxD - Transmit Data
DTR - Data Terminal Ready
SGND - Signal Ground
DSR - Data Set Ready
RTS - Request To Send
CTS - Clear To Send
RI - Ring Indicator

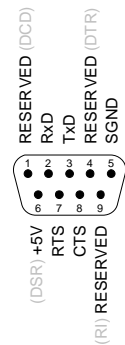
RS485 legend:

SHIGH - Signal HIGH
SGND - Signal Ground
SLOW - Signal LOW

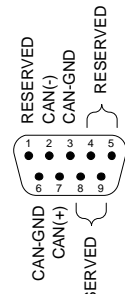


*****COM1, ***COM2
(RS485) (male)**

***with -COM extension

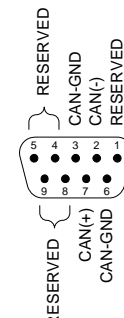


*****COM1, ***COM2
(RS232) (male)**

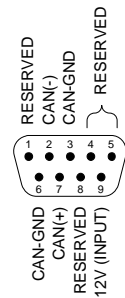


*****CAN 1.1 & ***CAN 2
(male)**

***with -COM extension

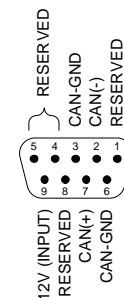


*****CAN 1.2**



*****CAN-LS 1.1 & 2
(male)**

***with -COM extension

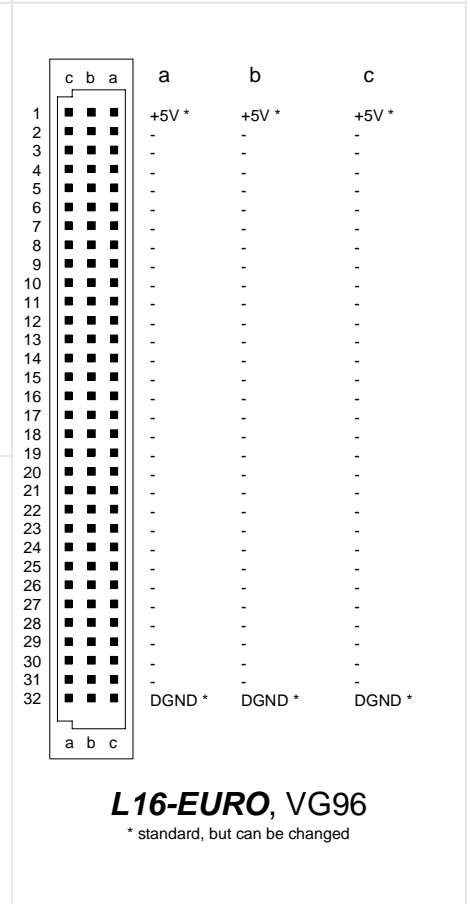
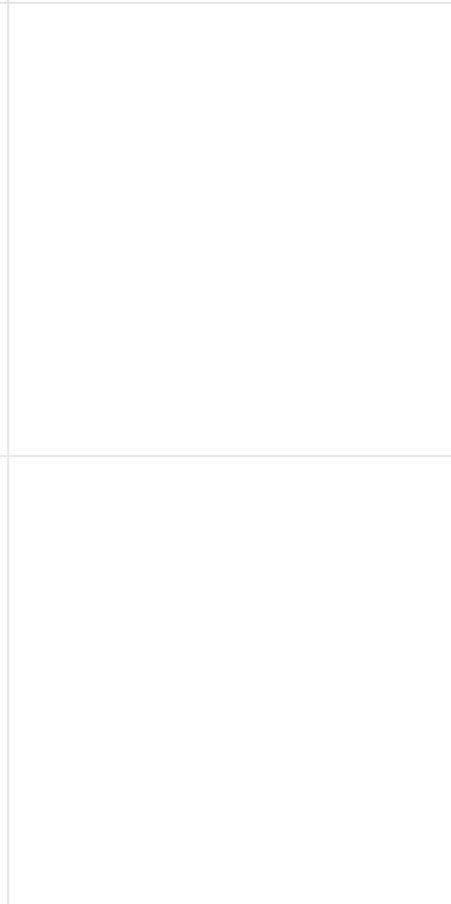
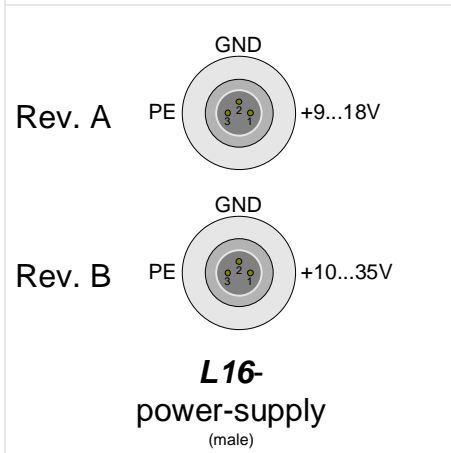
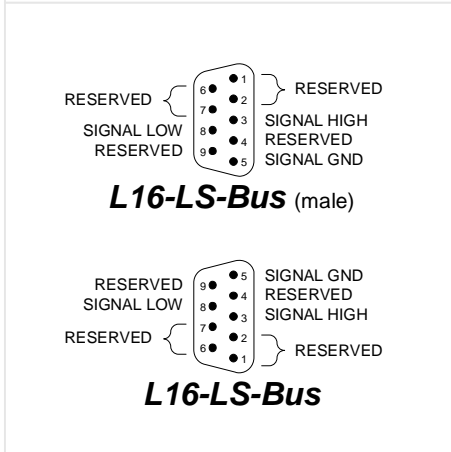
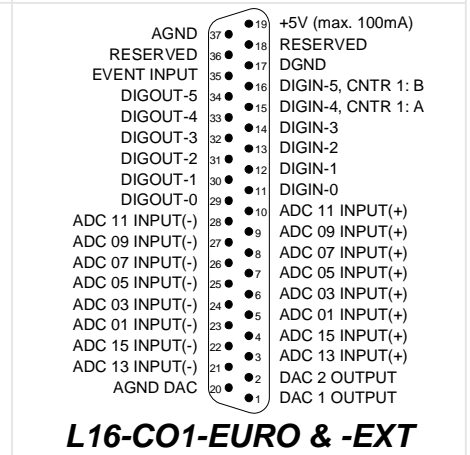
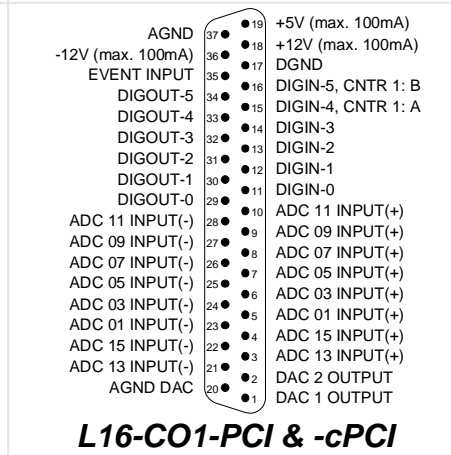
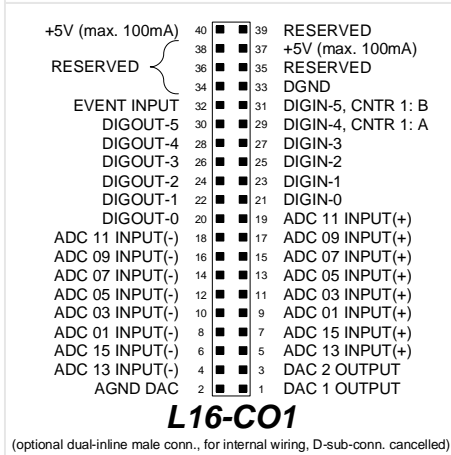
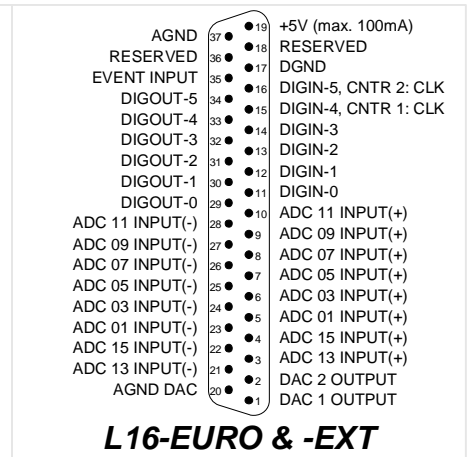
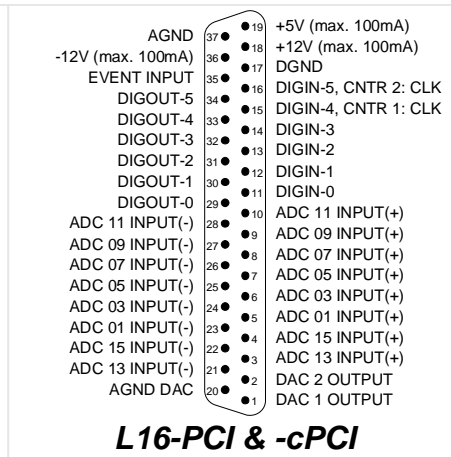
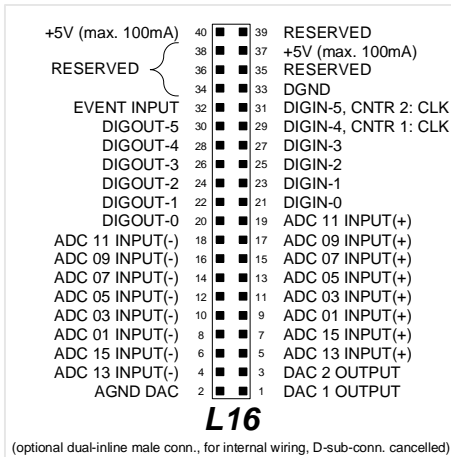


*****CAN-LS 1.2**

If not otherwise noted, all connectors are of female type.

ADwin-light-16 (part 1)

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If not otherwise noted, all connectors are of female type.

ADwin-light-16 (part 2)

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	<p>AGND -12V (max. 100mA) EVENT INPUT DIGOUT-5 DIGOUT-4 DIGOUT-3 DIGOUT-2 DIGOUT-1 DIGOUT-0 ADC 11 INPUT (-) ADC 09 INPUT (-) ADC 07 INPUT (-) ADC 05 INPUT (-) ADC 03 INPUT (-) ADC 01 INPUT (-) ADC 15 INPUT (-) ADC 13 INPUT (-) AGND DAC</p> <p>+5V (max. 100mA) +12V (max. 100mA) DGND DIGIN-5 DIGIN-4 DIGIN-3 DIGIN-2 DIGIN-1 DIGIN-0 ADC 11 INPUT (+) ADC 09 INPUT (+) ADC 07 INPUT (+) ADC 05 INPUT (+) ADC 03 INPUT (+) ADC 01 INPUT (+) ADC 15 INPUT (+) ADC 13 INPUT (+) DAC 2 OUTPUT DAC 1 OUTPUT</p> <p>L16-PCI & -cPCI</p>	<p>AGND RESERVED EVENT INPUT DIGOUT-5 DIGOUT-4 DIGOUT-3 DIGOUT-2 DIGOUT-1 DIGOUT-0 ADC 11 INPUT (-) ADC 09 INPUT (-) ADC 07 INPUT (-) ADC 05 INPUT (-) ADC 03 INPUT (-) ADC 01 INPUT (-) ADC 15 INPUT (-) ADC 13 INPUT (-) AGND DAC</p> <p>+5V (max. 100mA) RESERVED DGND DIGIN-5 DIGIN-4 DIGIN-3 DIGIN-2 DIGIN-1 DIGIN-0 ADC 11 INPUT (+) ADC 09 INPUT (+) ADC 07 INPUT (+) ADC 05 INPUT (+) ADC 03 INPUT (+) ADC 01 INPUT (+) ADC 15 INPUT (+) ADC 13 INPUT (+) DAC 2 OUTPUT DAC 1 OUTPUT</p> <p>L16-EURO & -EXT</p>
<p>L16-DIO1-EURO, VG96 * standard, but can be changed</p>	<p>L16-DIO1-Counter (diff.) (dual-inline male connector)</p>	<p>L16-DIO1-Counter (diff.) (dual-inline male connector)</p>
<p>L16-DIO1-CAN (dual-inline male connector)</p>	<p>L16-DIO1(DIO3), DIO (dual-inline male connector)</p>	<p>L16-DIO1(DIO3), DIO (dual-inline male connector)</p>

If not otherwise noted, all connectors are of female type.

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