ADwin-CO1

Counter expansion for *ADwin*-boards

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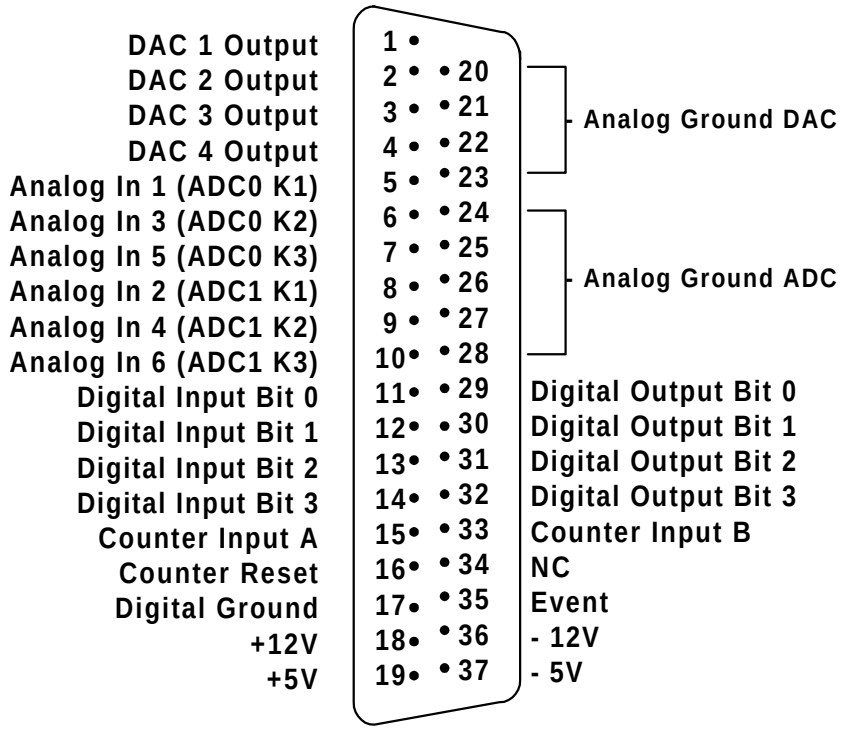
ADwin-CO1 counter expansion for *ADwin* Systems

Pin and address assignements



The 37-pin-D-socket at the rear of the card is equipped with the inputs for the up- and down counters instead of the digital inputs/outputs 4 and 5.
The TTL-inputs "Counter Input A" and "Counter Input B" are connected with the two quadrature encoders whose signals are shifted by 90 degrees.
A high pulse at the counter reset input clears the counter.

The ***ADwin***-card includes a 16-bit up/down counter. In order to control the counter 3 I/O-addresses are necessary. The Enable-register is set on address 0E0h. If writing 1 into the register the counter will be cleared. 2 starts the counter and 0 stops it. The Latch-register is on address 0F0h. If writing 1 into this register the present count rate is latched. The counter does not stop counting during this process. The latch is read out on address 0D0h.

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Rem Sample program for reading out the 16-bit V/R-counter
dim CO_ENABL, CO_LATCH, CO_REG as integer
dim  co as integer

init:
CO_ENABL = 0E0H
CO_LATCH = 0F0H
CO_REG   = 0D0H
poke CO_ENABL, 1  ' clear counter
poke CO_ENABL, 2  ' start counter

event:
  poke CO_LATCH,1    ' latch count rate
  co = peek(CO_REG)  ' read out latch and write into PAR-1
  par_1 = co
```