

ADwin - connector pin assignments

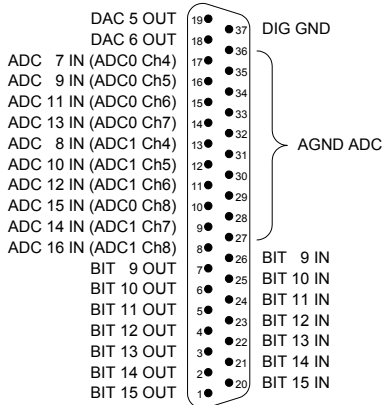
last update (yyyy-mm-dd): 2004-06-16

On the following pages you will find the connector-pinouts for all **ADwin**-cards, **ADwin-Pro**-, **ADwin-GOLD**- and **ADwin-L16**-systems.

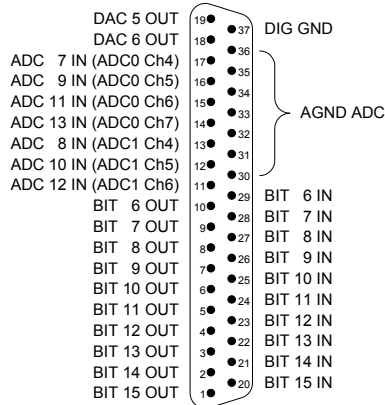
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ADwin - analog-/digital-cards

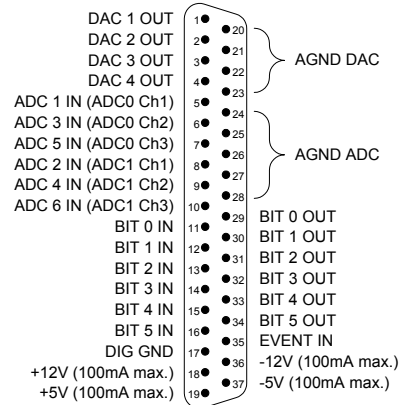
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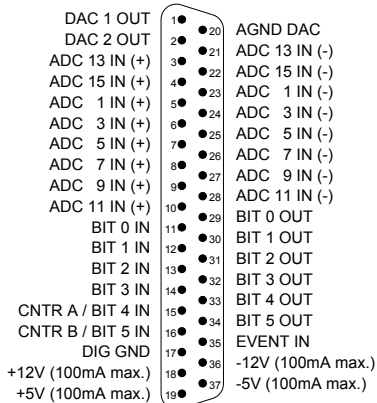
ADwin add-on connector (16 AIN / 26 DIO)



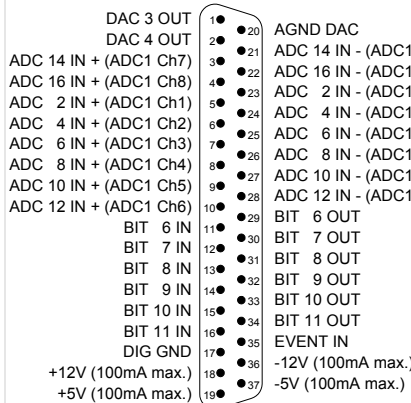
ADwin add-on connector (13 AIN / 32 DIO)



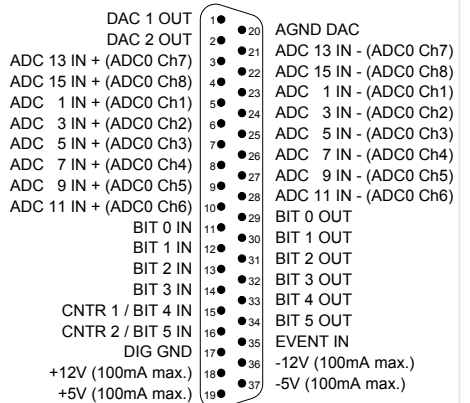
ADwin



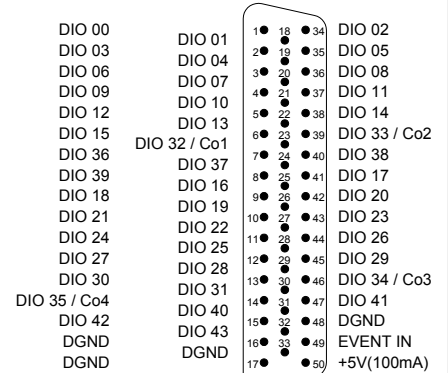
ADwin-CO1L



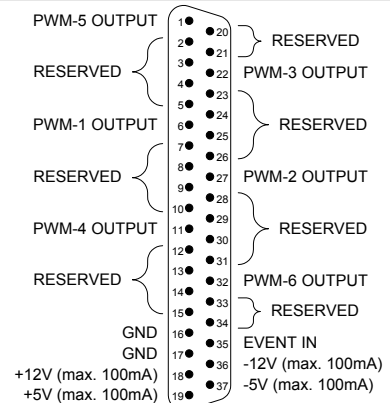
ADwin-ADL



ADwin-light



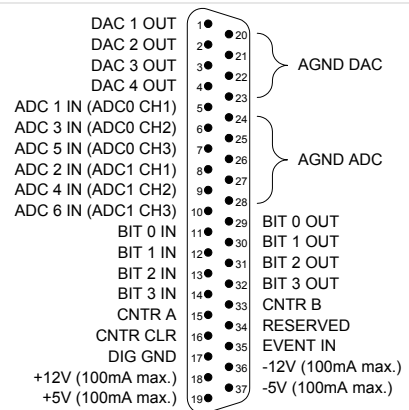
ADwin-LD



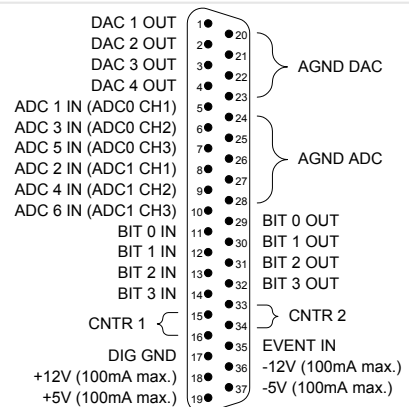
ADwin-PWM6

If not otherwise noted, all connectors are of female type.

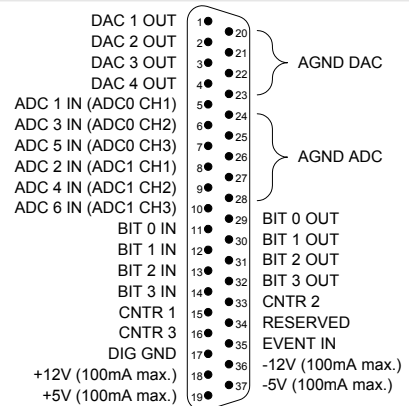
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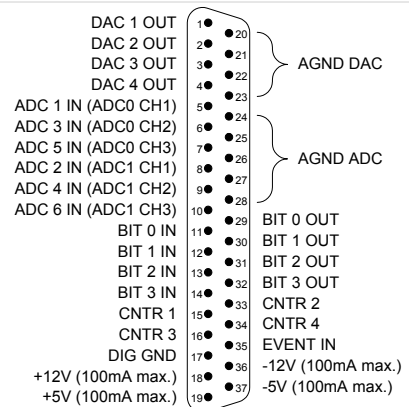
ADwin-CO1



ADwin-CO2



ADwin-CO3



ADwin-CO4

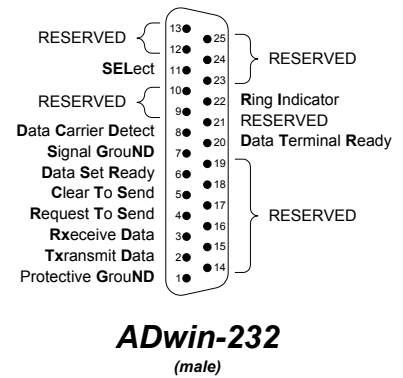
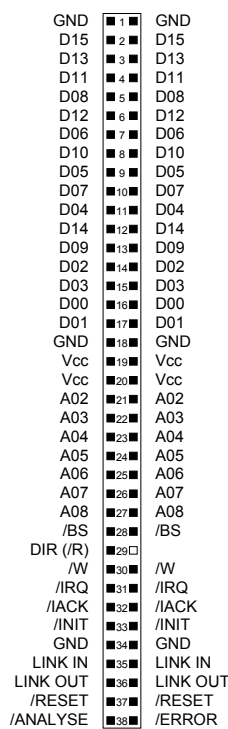
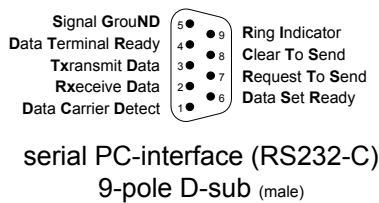
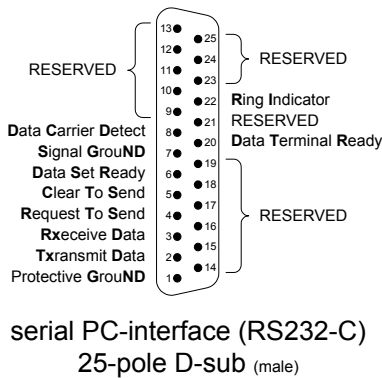
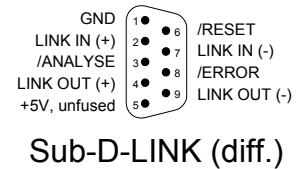
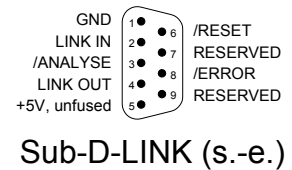
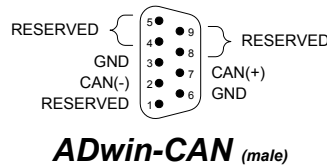
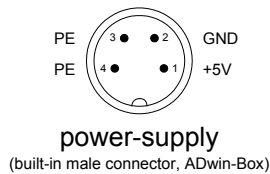
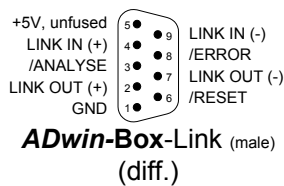
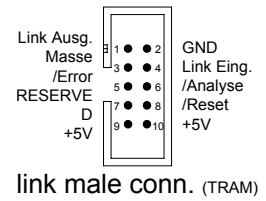
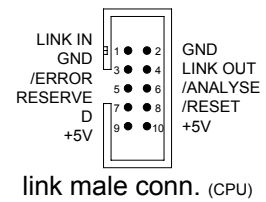
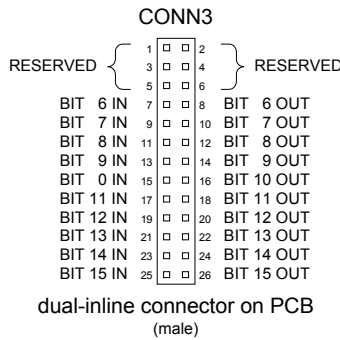
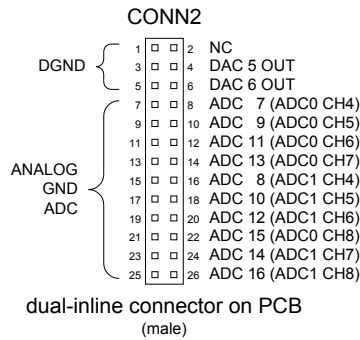
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ADwin - counter-cards

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<div><div><div>CNTR 5 LATCH</div><div>RESERVED</div><div>CNTR 1 LATCH</div><div>RESERVED</div><div>CNTR 4 LATCH</div><div>RESERVED</div><div>GND</div><div>+12V (100mA max.)</div><div>+5V (100mA max.)</div></div><div><div><div>1</div><div>2</div><div>3</div><div>4</div><div>5</div><div>6</div><div>7</div><div>8</div><div>9</div><div>10</div><div>11</div><div>12</div><div>13</div><div>14</div><div>15</div><div>16</div><div>17</div><div>18</div><div>19</div></div><div><div>20</div><div>21</div><div>22</div><div>23</div><div>24</div><div>25</div><div>26</div><div>27</div><div>28</div><div>29</div><div>30</div><div>31</div><div>32</div><div>33</div><div>34</div><div>35</div><div>36</div><div>37</div></div></div><div><div>RESERVED</div><div>CNTR 3 LATCH</div><div>RESERVED</div><div>CNTR 2 LATCH</div><div>RESERVED</div><div>CNTR 6 LATCH</div><div>RESERVED</div><div>EVENT IN</div><div>-12V (100mA max.)</div><div>-5V (100mA max.)</div></div></div> <div>ADwin-CO6</div>	<div><div><div>CNTR 9</div><div>RESERVED</div><div>CNTR 1</div><div>CNTR 2</div><div>RESERVED</div><div>CNTR 7</div><div>CNTR 8</div><div>RESERVED</div><div>GND</div><div>+12V (100mA max.)</div><div>+5V (100mA max.)</div></div><div><div><div>1</div><div>2</div><div>3</div><div>4</div><div>5</div><div>6</div><div>7</div><div>8</div><div>9</div><div>10</div><div>11</div><div>12</div><div>13</div><div>14</div><div>15</div><div>16</div><div>17</div><div>18</div><div>19</div></div><div><div>20</div><div>21</div><div>22</div><div>23</div><div>24</div><div>25</div><div>26</div><div>27</div><div>28</div><div>29</div><div>30</div><div>31</div><div>32</div><div>33</div><div>34</div><div>35</div><div>36</div><div>37</div></div></div><div><div>RESERVED</div><div>CNTR 5</div><div>CNTR 6</div><div>RESERVED</div><div>CNTR 3</div><div>CNTR 4</div><div>RESERVED</div><div>CNTR 11</div><div>CNTR 12</div><div>RESERVED</div><div>EVENT IN</div><div>-12V (100mA max.)</div><div>-5V (100mA max.)</div></div></div> <div>ADwin-CO12</div>	<div><div><div>CNTR 17</div><div>CNTR 18</div><div>RESERVED</div><div>CNTR 11</div><div>CNTR 12</div><div>CNTR 1</div><div>CNTR 2</div><div>RESERVED</div><div>CNTR 7</div><div>CNTR 8</div><div>CNTR 13</div><div>CNTR 14</div><div>RESERVED</div><div>CNTR 23</div><div>CNTR 24</div><div>GND</div><div>+12V (100mA max.)</div><div>+5V (100mA max.)</div></div><div><div><div>1</div><div>2</div><div>3</div><div>4</div><div>5</div><div>6</div><div>7</div><div>8</div><div>9</div><div>10</div><div>11</div><div>12</div><div>13</div><div>14</div><div>15</div><div>16</div><div>17</div><div>18</div><div>19</div></div><div><div>20</div><div>21</div><div>22</div><div>23</div><div>24</div><div>25</div><div>26</div><div>27</div><div>28</div><div>29</div><div>30</div><div>31</div><div>32</div><div>33</div><div>34</div><div>35</div><div>36</div><div>37</div></div></div><div><div>CNTR 19</div><div>CNTR 20</div><div>CNTR 9</div><div>CNTR 10</div><div>RESERVED</div><div>CNTR 3</div><div>CNTR 4</div><div>CNTR 5</div><div>CNTR 6</div><div>RESERVED</div><div>CNTR 15</div><div>CNTR 16</div><div>CNTR 21</div><div>CNTR 22</div><div>RESERVED</div><div>EVENT IN</div><div>-12V (100mA max.)</div><div>-5V (100mA max.)</div></div></div> <div>ADwin-CO24</div>
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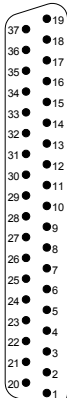
If not otherwise noted, all connectors are of female type.



ADwin-Pro - ADC/DAC-modules

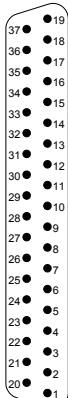
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ANALOG IN 17
ANALOG IN 18
ANALOG IN 19
ANALOG IN 20
ANALOG IN 21
ANALOG IN 22
ANALOG IN 23
ANALOG IN 24
ANALOG IN 25
ANALOG IN 26
ANALOG IN 27
ANALOG IN 28
ANALOG IN 29
ANALOG IN 30
ANALOG IN 31
ANALOG IN 32
AGND
EVENT IN



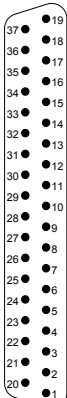
AIn-32/1x: -SE (with x=2, 4, 6)

ANALOG IN 1 (-)
ANALOG IN 2 (-)
ANALOG IN 3 (-)
ANALOG IN 4 (-)
ANALOG IN 5 (-)
ANALOG IN 6 (-)
ANALOG IN 7 (-)
ANALOG IN 8 (-)
ANALOG IN 17 (-)
ANALOG IN 18 (-)
ANALOG IN 19 (-)
ANALOG IN 20 (-)
ANALOG IN 21 (-)
ANALOG IN 22 (-)
ANALOG IN 23 (-)
ANALOG IN 24 (-)
AGND
EVENT IN

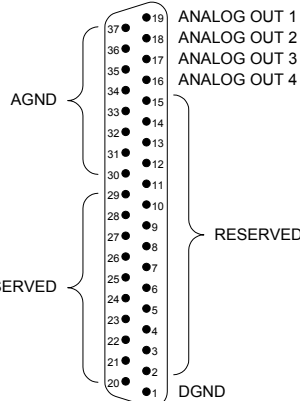


AIn-32/1x: -Diff (with x=2, 4, 6)

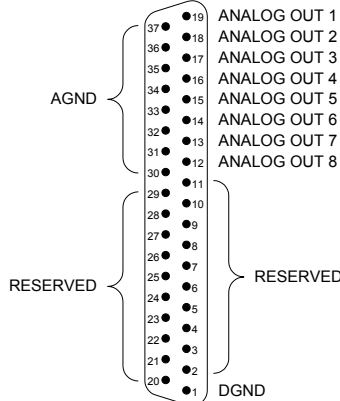
ANALOG IN 1 (-)
ANALOG IN 2 (-)
ANALOG IN 3 (-)
ANALOG IN 4 (-)
ANALOG IN 5 (-)
ANALOG IN 6 (-)
ANALOG IN 7 (-)
ANALOG IN 8 (-)
RESERVED
AGND
RESERVED



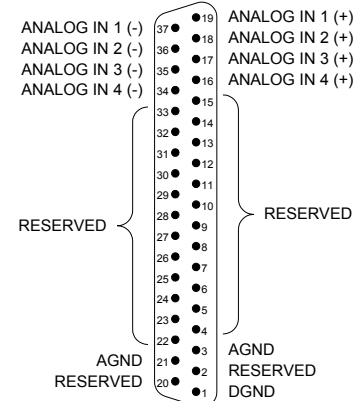
AIn-8/1x-D (with x=2, 4, 6)



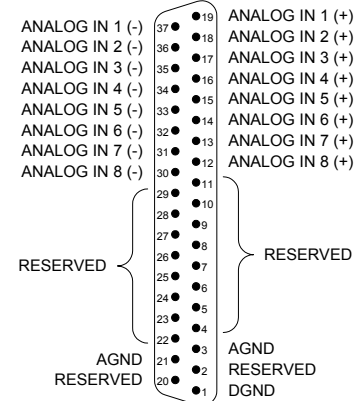
AOut-4/16-D



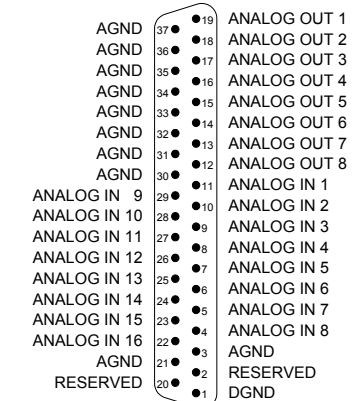
AOut-8/16-D



AIn-F-4/1x-D (with x=2, 4, 6)



AIn-F-8/1x-D (with x=2, 4, 6)



AO-16/8-12

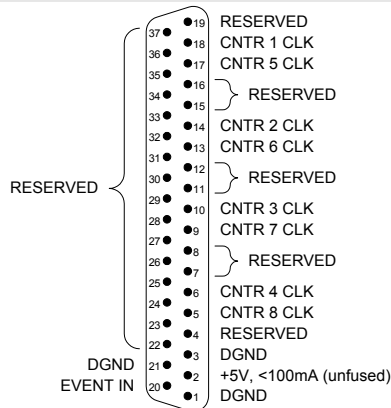
If not otherwise noted, all connectors are of female type.

ADwin-Pro - DIO-modules

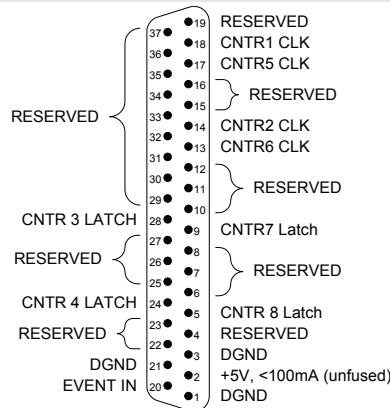
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<p>DIG I/O, BIT 1 DIG I/O, BIT 3 DIG I/O, BIT 5 DIG I/O, BIT 7 DIG I/O, BIT 9 DIG I/O, BIT 11 DIG I/O, BIT 13 DIG I/O, BIT 15 DIG I/O, BIT 17 DIG I/O, BIT 19 DIG I/O, BIT 21 DIG I/O, BIT 23 DIG I/O, BIT 25 DIG I/O, BIT 27 DIG I/O, BIT 29 DIG I/O, BIT 31 DGND EVENT IN</p> <p>DIO-32</p>	<p>RELAY 0 A RELAY 1 A RELAY 2 A RELAY 3 A RELAY 4 A RELAY 5 A RELAY 6 A RELAY 7 A RELAY 8 A RELAY 9 A RELAY 10 A RELAY 11 A RELAY 12 A RELAY 13 A RELAY 14 A RELAY 15 A DGND RESERVED EVENT IN (+)</p> <p>RELAY 0 B RELAY 1 B RELAY 2 B RELAY 3 B RELAY 4 B RELAY 5 B RELAY 6 B RELAY 7 B RELAY 8 B RELAY 9 B RELAY 10 B RELAY 11 B RELAY 12 B RELAY 13 B RELAY 14 B RELAY 15 B DGND RESERVED EVENT IN (-)</p> <p>REL-16</p>	<p>EMITTER 0 EMITTER 1 EMITTER 2 EMITTER 3 EMITTER 4 EMITTER 5 EMITTER 6 EMITTER 7 EMITTER 8 EMITTER 9 EMITTER 10 EMITTER 11 EMITTER 12 EMITTER 13 EMITTER 14 EMITTER 15 DGND RESERVED EVENT IN (-)</p> <p>External GND</p> <p>External Vcc</p> <p>DGND EVENT IN (+)</p> <p>TRA-16</p>
<p>Dig. Ein-/Ausg., Bit 1 Dig. Ein-/Ausg., Bit 3 Dig. Ein-/Ausg., Bit 5 Dig. Ein-/Ausg., Bit 7 Dig. Ein-/Ausg., Bit 9 Dig. Ein-/Ausg., Bit 11 Dig. Ein-/Ausg., Bit 13 Dig. Ein-/Ausg., Bit 15 Dig. Ein-/Ausg., Bit 17 Dig. Ein-/Ausg., Bit 19 Dig. Ein-/Ausg., Bit 21 Dig. Ein-/Ausg., Bit 23 Dig. Ein-/Ausg., Bit 25 Dig. Ein-/Ausg., Bit 27 Dig. Ein-/Ausg., Bit 29 Dig. Ein-/Ausg., Bit 31 DGND EVENT-Eingang</p> <p>Dig. Ein-/Ausg., Bit 0 Dig. Ein-/Ausg., Bit 2 Dig. Ein-/Ausg., Bit 4 Dig. Ein-/Ausg., Bit 6 Dig. Ein-/Ausg., Bit 8 Dig. Ein-/Ausg., Bit 10 Dig. Ein-/Ausg., Bit 12 Dig. Ein-/Ausg., Bit 14 Dig. Ein-/Ausg., Bit 16 Dig. Ein-/Ausg., Bit 18 Dig. Ein-/Ausg., Bit 20 Dig. Ein-/Ausg., Bit 22 Dig. Ein-/Ausg., Bit 24 Dig. Ein-/Ausg., Bit 26 Dig. Ein-/Ausg., Bit 28 Dig. Ein-/Ausg., Bit 30 +5V, <100mA (ges.) DGND</p> <p>DIO-32-RB</p>	<p>PWM OUTPUT 1 (+) RESERVED PWM OUTPUT 2 (+) RESERVED PWM OUTPUT 3 (+) RESERVED PWM OUTPUT 4 (+) RESERVED EVENT IN (-)</p> <p>External GND</p> <p>External Vcc</p> <p>RESERVED EVENT IN (+)</p> <p>PWM-4-I</p>	<p>RESERVED PWM OUTPUT 1 RESERVED PWM OUTPUT 2 RESERVED PWM OUTPUT 3 RESERVED PWM OUTPUT 4 RESERVED DGND RESERVED DGND</p> <p>RESERVED</p> <p>DGND EVENT IN</p> <p>PWM-4</p>
<p>INPUT 0 INPUT 1 INPUT 2 INPUT 3 INPUT 4 INPUT 5 INPUT 6 INPUT 7 INPUT 8 INPUT 9 INPUT 10 INPUT 11 INPUT 12 INPUT 13 INPUT 14 INPUT 15 DGND RESERVED DGND</p> <p>DGND</p> <p>RESERVED</p> <p>Comp-16</p>		<p>DIG IN, BIT 0 (+) DIG IN, BIT 1 (+) DIG IN, BIT 2 (+) DIG IN, BIT 3 (+) DIG IN, BIT 4 (+) DIG IN, BIT 5 (+) DIG IN, BIT 6 (+) DIG IN, BIT 7 (+) DIG IN, BIT 8 (+) DIG IN, BIT 9 (+) DIG IN, BIT 10 (+) DIG IN, BIT 11 (+) DIG IN, BIT 12 (+) DIG IN, BIT 13 (+) DIG IN, BIT 14 (+) DIG IN, BIT 15 (+) DGND RESERVED EVENT IN (-)</p> <p>DIG IN, BIT 0 (-) DIG IN, BIT 1 (-) DIG IN, BIT 2 (-) DIG IN, BIT 3 (-) DIG IN, BIT 4 (-) DIG IN, BIT 5 (-) DIG IN, BIT 6 (-) DIG IN, BIT 7 (-) DIG IN, BIT 8 (-) DIG IN, BIT 9 (-) DIG IN, BIT 10 (-) DIG IN, BIT 11 (-) DIG IN, BIT 12 (-) DIG IN, BIT 13 (-) DIG IN, BIT 14 (-) DIG IN, BIT 15 (-) DGND EVENT IN (+)</p> <p>OPT-16</p>
		<p>DIG IN, BIT 1 DIG IN, BIT 3 DIG IN, BIT 5 DIG IN, BIT 7 DIG IN, BIT 9 DIG IN, BIT 11 DIG IN, BIT 13 DIG IN, BIT 15 DIG IN, BIT 17 DIG IN, BIT 19 DIG IN, BIT 21 DIG IN, BIT 23 DIG IN, BIT 25 DIG IN, BIT 27 DIG IN, BIT 29 DIG IN, BIT 31 EXT. GND EVENT IN</p> <p>DIG IN, BIT 0 DIG IN, BIT 2 DIG IN, BIT 4 DIG IN, BIT 6 DIG IN, BIT 8 DIG IN, BIT 10 DIG IN, BIT 12 DIG IN, BIT 14 DIG IN, BIT 16 DIG IN, BIT 18 DIG IN, BIT 20 DIG IN, BIT 22 DIG IN, BIT 24 DIG IN, BIT 26 DIG IN, BIT 28 DIG IN, BIT 30 EXT. GND RESERVED EXT. GND</p> <p>OPT-32</p>

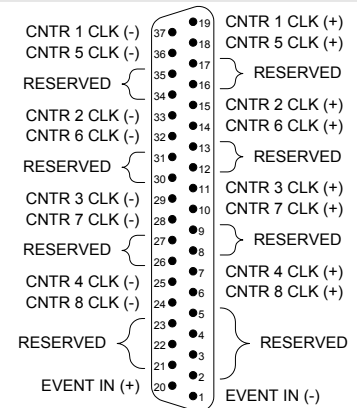
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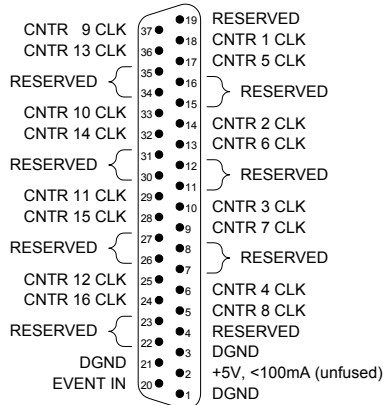
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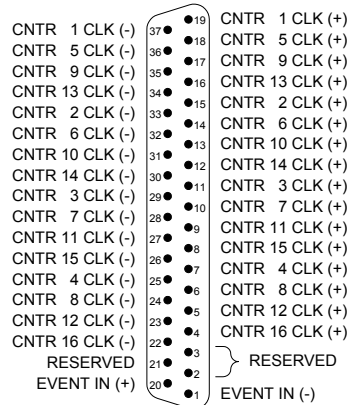
CNT-8/32-L



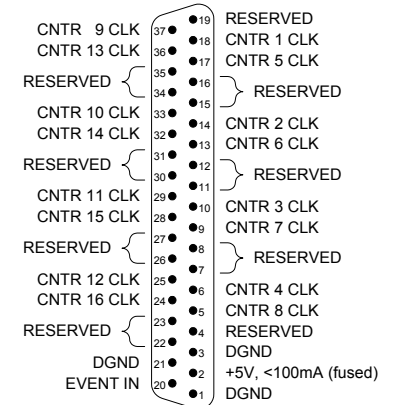
CNT-8/32-I



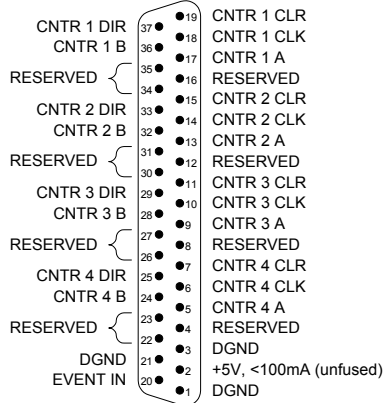
CNT-16/16



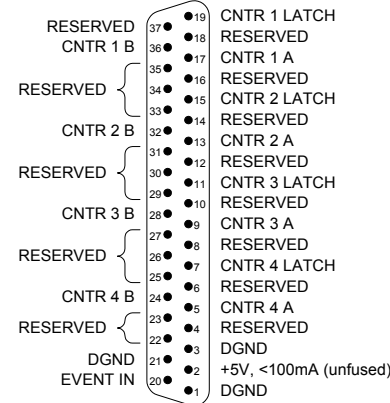
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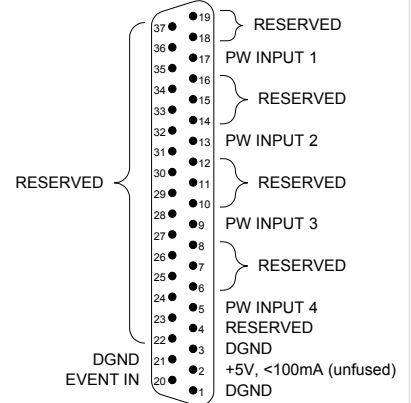
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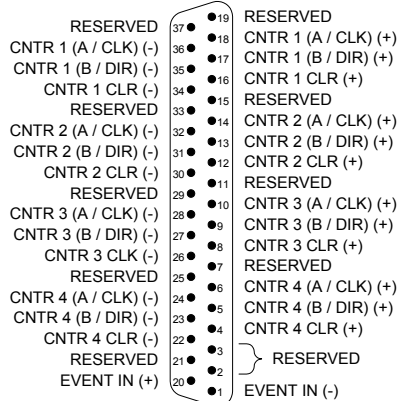
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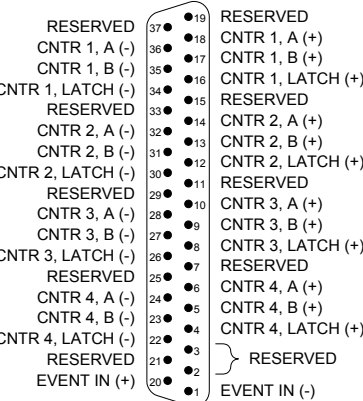
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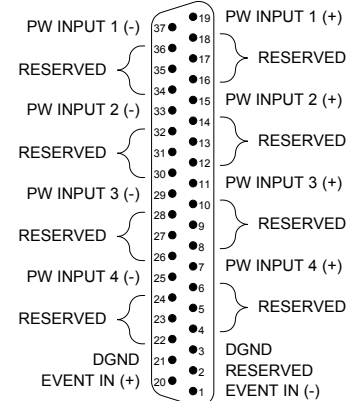
CNT-PW4



CNT-VR4-I



CNT-VR4-L-I



CNT-PW4-I

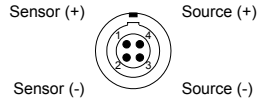
If not otherwise noted, all connectors are of female type.

ADwin-Pro - counter-modules (part 2)

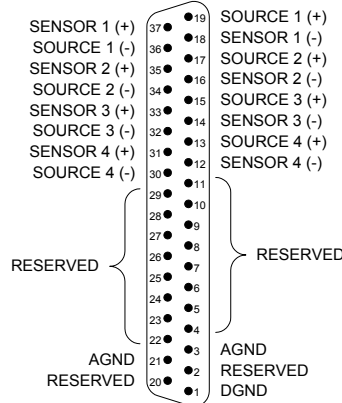
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	<p>RESERVED 37 ● 19 CNTR 1 (A / CLK) (-) 36 ● 18 CNTR 1 (B / DIR) (-) 35 ● 17 CNTR 1 CLR(-) 34 ● 16 RESERVED 33 ● 15 CNTR 2 (A / CLK) (-) 32 ● 14 CNTR 2 (B / DIR) (-) 31 ● 13 CNTR 2 CLR(-) 30 ● 12 PW-INPUT 3 (-) 29 ● 11 RESERVED 28 ● 10 RESERVED 27 ● 9 PW-INPUT 4 (-) 26 ● 8 RESERVED 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 RESERVED 22 ● 4 RESERVED 21 ● 3 EVENT-IN (+) 20 ● 2 1 ● 1</p> <p>RESERVED 37 ● 19 CNTR 1 (A / CLK) (+) 36 ● 18 CNTR 1 (B / DIR) (+) 35 ● 17 CNTR 1 CLR 34 ● 16 RESERVED 33 ● 15 CNTR 2 (A / CLK) (+) 32 ● 14 CNTR 2 (B / DIR) (+) 31 ● 13 CNTR 2 CLR(+) 30 ● 12 PW-INPUT 3 (+) 29 ● 11 RESERVED 28 ● 10 RESERVED 27 ● 9 PW-INPUT 4 (+) 26 ● 8 RESERVED 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 RESERVED 22 ● 4 RESERVED 21 ● 3 EVENT-IN (-) 20 ● 2 1 ● 1</p> <p>CNT-VR2PW2-I</p>	<p>CNTR 1 DIR 37 ● 19 CNTR 1 B 36 ● 18 RESERVED 35 ● 17 CNTR 2 DIR 34 ● 16 CNTR 2 B 33 ● 15 RESERVED 32 ● 14 RESERVED 31 ● 13 RESERVED 30 ● 12 RESERVED 29 ● 11 RESERVED 28 ● 10 RESERVED 27 ● 9 RESERVED 26 ● 8 RESERVED 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 RESERVED 22 ● 4 DGND 21 ● 3 EVENT IN 20 ● 2 1 ● 1</p> <p>CNTR 1 CLR CNTR1 CLK CNTR1 A RESERVED CNTR2 CLR CNTR2 CLK CNTR2 A RESERVED PW INPUT 3 RESERVED PW INPUT 4 RESERVED DGND +5V, <100mA (unfused) DGND</p> <p>CNT-VR2PW2</p>
<p>RESERVED 37 ● 19 CNTR 1, A/CLK/PWM (-) 36 ● 18 CNTR 1, B/DIR (-) 35 ● 17 CNTR 1, CLR/LATCH (-) 34 ● 16 RESERVED 33 ● 15 CNTR 2, A/CLK/PWM (-) 32 ● 14 CNTR 2, B/DIR (-) 31 ● 13 CNTR 2, CLR/LATCH (-) 30 ● 12 RESERVED 29 ● 11 CNTR 3, A/CLK/PWM (-) 28 ● 10 CNTR 3, B/DIR (-) 27 ● 9 CNTR 3, CLR/LATCH (-) 26 ● 8 RESERVED 25 ● 7 CNTR 4, A/CLK/PWM (-) 24 ● 6 CNTR 4, B/DIR (-) 23 ● 5 CNTR 4, CLR/LATCH (-) 22 ● 4 RESERVED 21 ● 3 EVENT-IN (+) 20 ● 2 1 ● 1</p> <p>RESERVED 37 ● 19 CNTR 1, A/CLK/PWM (+) 36 ● 18 CNTR 1, B/DIR (+) 35 ● 17 CNTR 1, CLR/LATCH (+) 34 ● 16 RESERVED 33 ● 15 CNTR 2, A/CLK/PWM (+) 32 ● 14 CNTR 2, B/DIR (+) 31 ● 13 CNTR 2, CLR/LATCH (+) 30 ● 12 RESERVED 29 ● 11 CNTR 3, A/CLK/PWM (+) 28 ● 10 CNTR 3, B/DIR (+) 27 ● 9 CNTR 3, CLR/LATCH (+) 26 ● 8 RESERVED 25 ● 7 CNTR 4, A/CLK/PWM (+) 24 ● 6 CNTR 4, B/DIR (+) 23 ● 5 CNTR 4, CLR/LATCH (+) 22 ● 4 RESERVED 21 ● 3 EVENT-IN (-) 20 ● 2 1 ● 1</p> <p>CO4-I</p>	<p>SSI 1, CLK (-) 37 ● 19 CNTR 1, A/CLK/PWM (-) 36 ● 18 CNTR 1, B/DIR (-) 35 ● 17 CNTR 1, CLR/LATCH (-) 34 ● 16 SSI 1, DATA (-) 33 ● 15 CNTR 2, A/CLK/PWM (-) 32 ● 14 CNTR 2, B/DIR (-) 31 ● 13 CNTR 2, CLR/LATCH (-) 30 ● 12 SSI 2, CLK (-) 29 ● 11 CNTR 3, A/CLK/PWM (-) 28 ● 10 CNTR 3, B/DIR (-) 27 ● 9 CNTR 3, CLR/LATCH (-) 26 ● 8 SSI 2, DATA (-) 25 ● 7 CNTR 4, A/CLK/PWM (-) 24 ● 6 CNTR 4, B/DIR (-) 23 ● 5 CNTR 4, CLR/LATCH (-) 22 ● 4 DGND 21 ● 3 EVENT-IN (+) 20 ● 2 1 ● 1</p> <p>SSI 1, CLK (+) 37 ● 19 CNTR 1, A/CLK/PWM (+) 36 ● 18 CNTR 1, B/DIR (+) 35 ● 17 CNTR 1, CLR/LATCH (+) 34 ● 16 SSI 1, DATA (+) 33 ● 15 CNTR 2, A/CLK/PWM (+) 32 ● 14 CNTR 2, B/DIR (+) 31 ● 13 CNTR 2, CLR/LATCH (+) 30 ● 12 SSI 2, CLK (+) 29 ● 11 CNTR 3, A/CLK/PWM (+) 28 ● 10 CNTR 3, B/DIR (+) 27 ● 9 CNTR 3, CLR/LATCH (+) 26 ● 8 SSI 2, DATA (+) 25 ● 7 CNTR 4, A/CLK/PWM (+) 24 ● 6 CNTR 4, B/DIR (+) 23 ● 5 CNTR 4, CLR/LATCH (+) 22 ● 4 DGND 21 ● 3 +5V, <100mA (fused) 20 ● 2 EVENT-IN (-) 1 ● 1</p> <p>CO4-D</p>	<p>CNTR 1 DIR 37 ● 19 CNTR 1 B 36 ● 18 RESERVED 35 ● 17 CNTR 2 DIR 34 ● 16 CNTR 2 B 33 ● 15 RESERVED 32 ● 14 RESERVED 31 ● 13 RESERVED 30 ● 12 CNTR 3 DIR 29 ● 11 CNTR 3 B 28 ● 10 RESERVED 27 ● 9 CNTR 4 DIR 26 ● 8 CNTR 4 B 25 ● 7 RESERVED 24 ● 6 RESERVED 23 ● 5 DGND 22 ● 4 EVENT-IN 21 ● 3 20 ● 2 1 ● 1</p> <p>CNTR 1 CLR/LATCH CNTR 1 CLK/PWM CNTR 1 A RESERVED CNTR 2 CLR/LATCH CNTR 2 CLK/PWM CNTR 2 A RESERVED CNTR 3 CLR/LATCH CNTR 3 CLK/PWM CNTR 3 A RESERVED CNTR 4 CLR/LATCH CNTR 4 CLK/PWM CNTR 4 A RESERVED DGND +5V, <100mA (fused) DGND</p> <p>CO4-T</p>

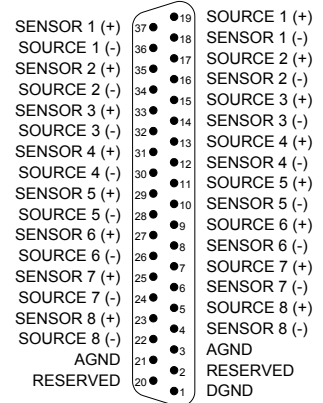
If not otherwise noted, all connectors are of female type.



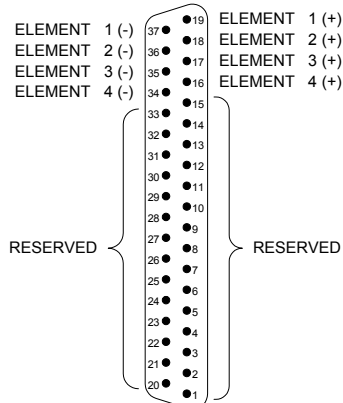
PT100



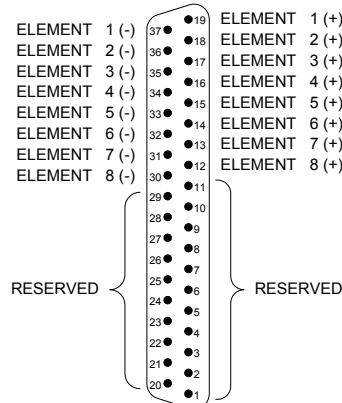
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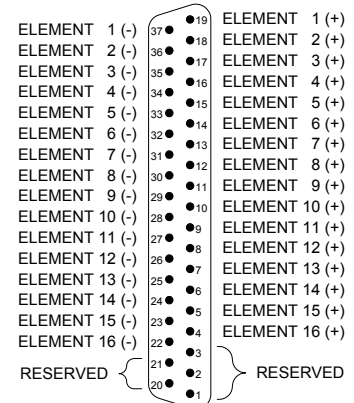
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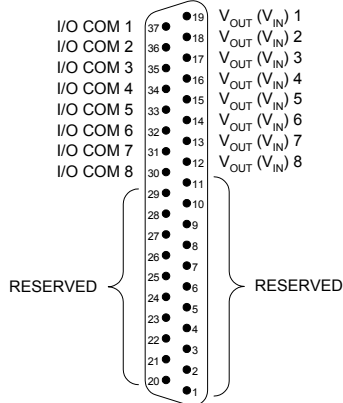
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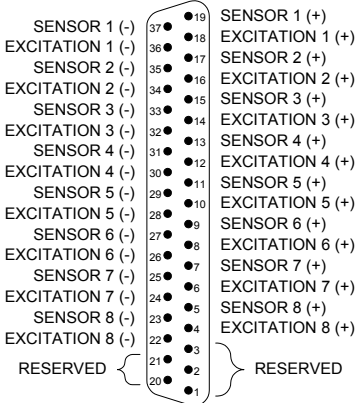
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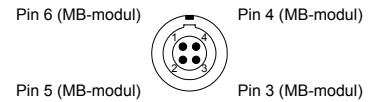
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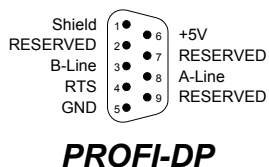
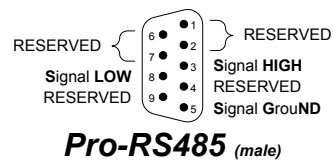
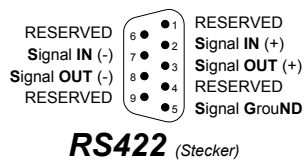
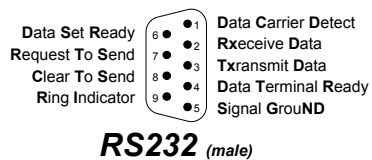
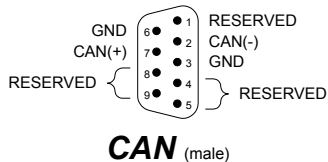
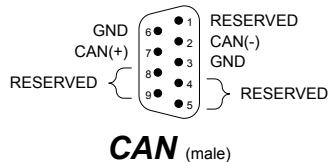
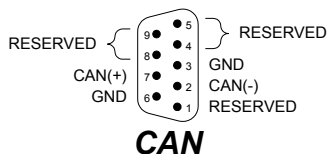
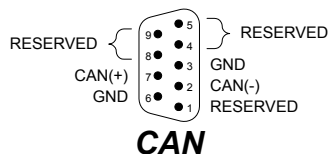
MB-8 Module-Output



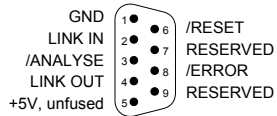
MB-8 Module-Input



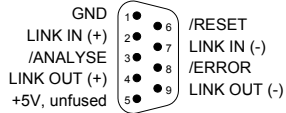
MB-8



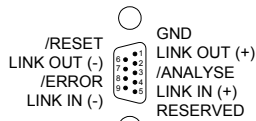
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D-sub-LINK (s.-e.)

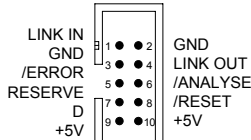


D-sub-LINK (diff.)



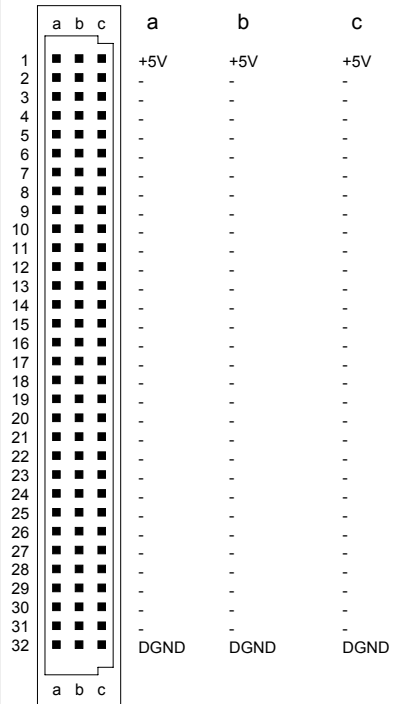
Link

(built-in male connector)

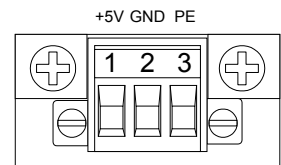


link-connector (CPU)

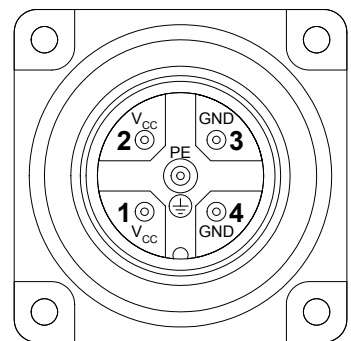
(male)



**ADwin-Pro, VG96
(Backplane)**



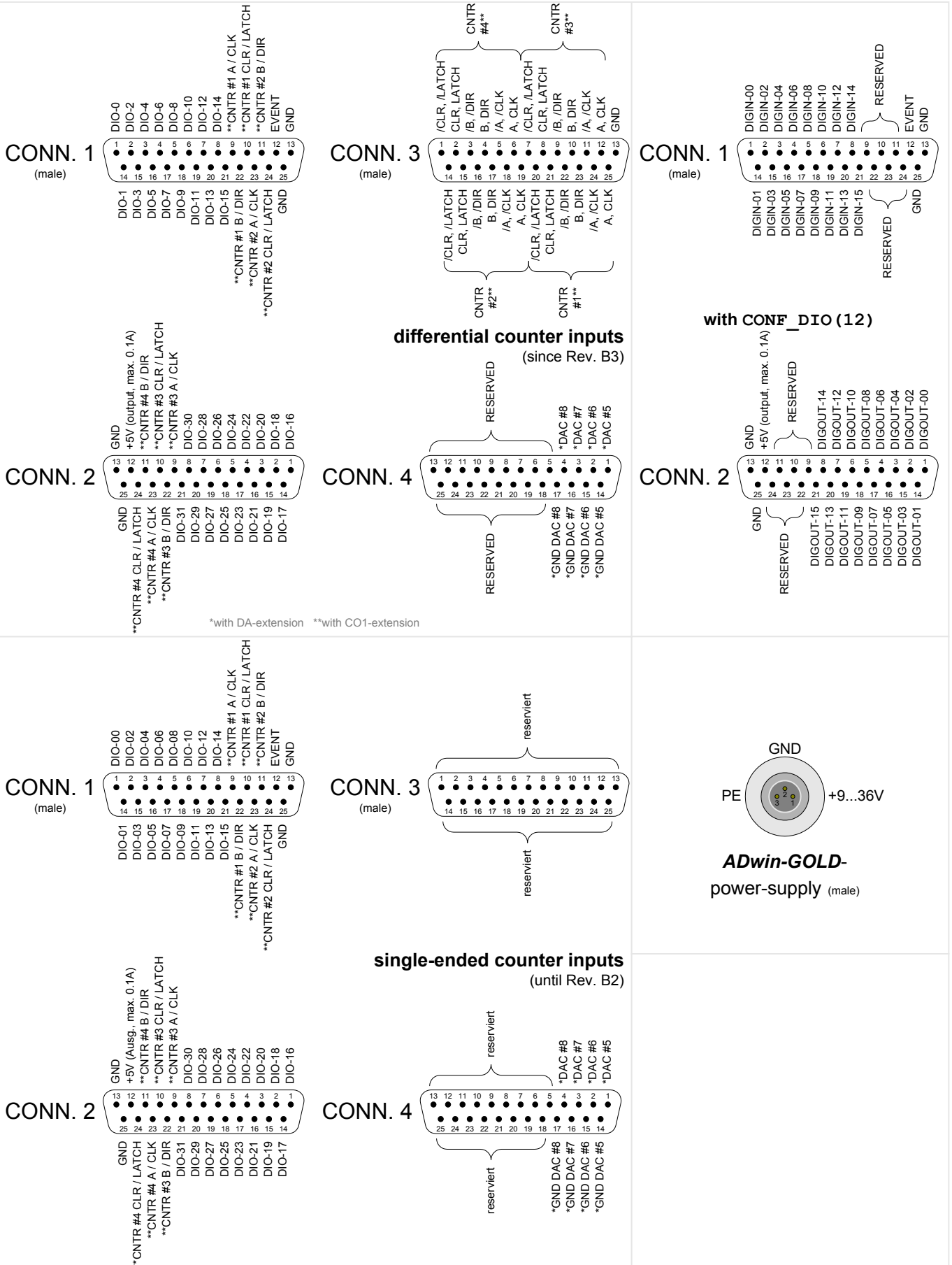
Pro-Mini (power-supply)



Pro-DC (power-supply)
(male)

If not otherwise noted, all connectors are of female type.

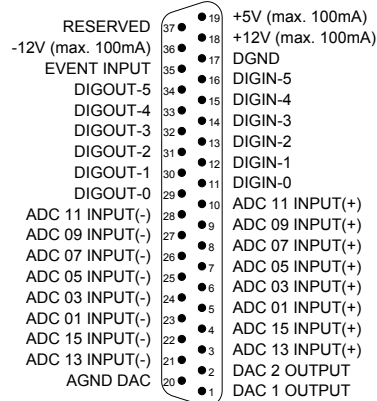
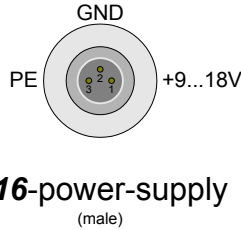




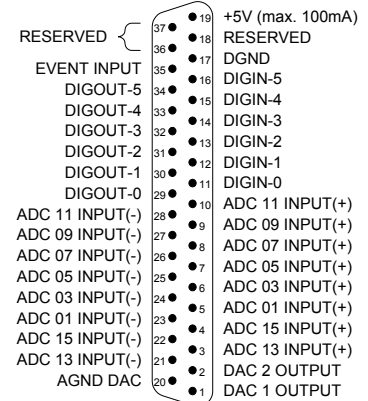
If not otherwise noted, all connectors are of female type.

<p>ANALOG OUT <small>*with -DA extension</small></p>	<p>ANALOG IN</p>	<p>ADwin-Gold- power-supply (male)</p>
<p>DIO 00-15 (IN) (male)</p> <p><small>**with -CO1 extension</small></p>	<p>DIO 00-15 (IN) (male)</p> <p><small>with CONF_DIO (12)</small></p>	<p>**CO POWER IN</p>
<p>DIO 16-31 (OUT)</p> <p><small>**with -CO1 extension</small></p>	<p>DIO 16-31 (OUT)</p> <p><small>with CONF_DIO (12)</small></p>	<p>**CO1, ..., CO4 (male)</p> <p><small>**with -CO1 extension, ***with -COM extension</small></p>
<p>***CAN 1.1 & ***CAN 2 (male)</p> <p><small>***with -COM extension</small></p>	<p>***CAN 1.2</p>	<p>***COM1, ***COM2 (RS485) (male)</p> <p>***COM1, ***COM2 (RS232) (male)</p> <p><small>***with -COM extension</small></p>

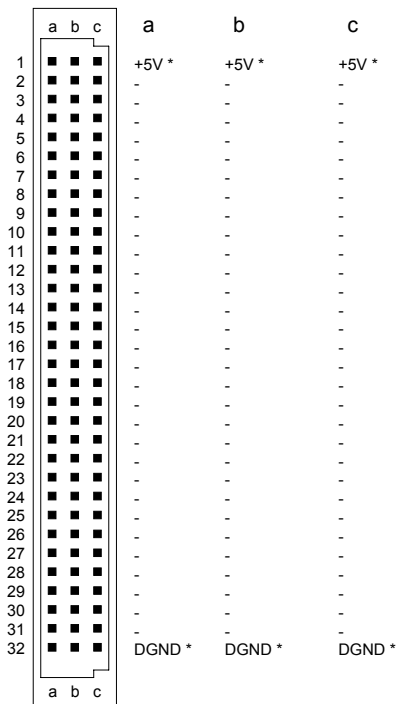
If not otherwise noted, all connectors are of female type.



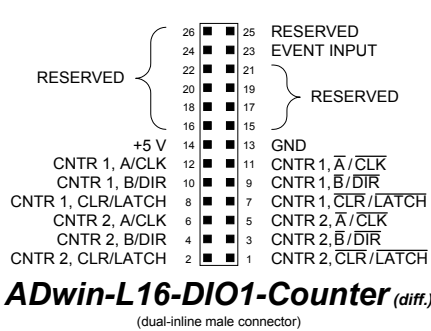
ADwin-L16-PCI & -cPCI



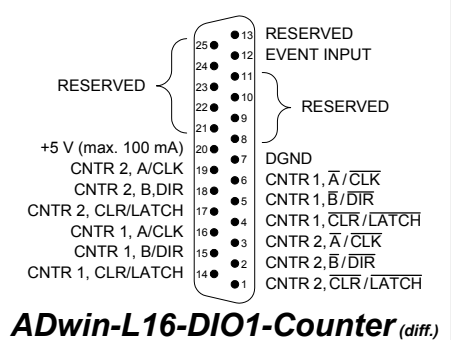
ADwin-L16-EURO & -EXT



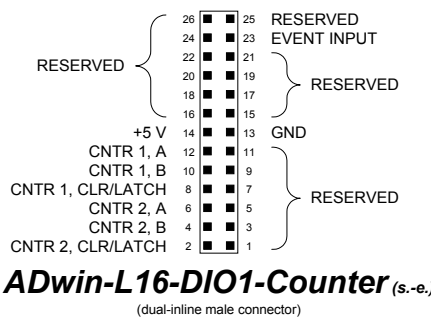
ADwin-L16-DIO1-EURO,
VG96, * standard, but can be changed



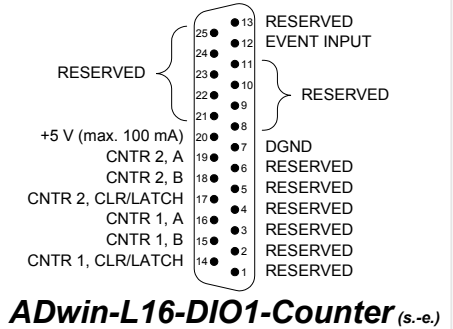
ADwin-L16-DIO1-Counter (diff.)
(dual-inline male connector)



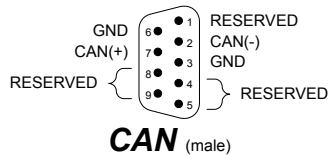
ADwin-L16-DIO1-Counter (diff.)



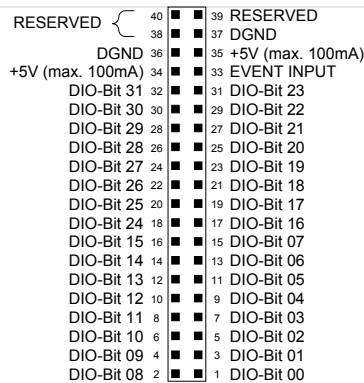
ADwin-L16-DIO1-Counter (s.-e.)
(dual-inline male connector)



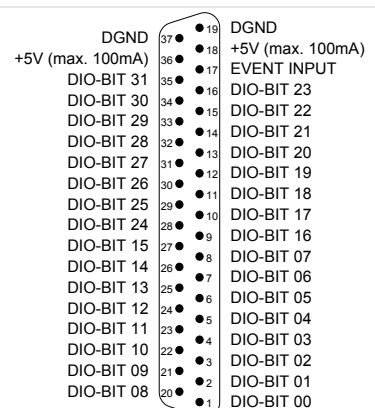
ADwin-L16-DIO1-Counter (s.-e.)



ADwin-L16-DIO1-CAN
(dual-inline male connector)



ADwin-L16-DIO1-DIO
(dual-inline male connector)



ADwin-L16-DIO1-DIO

If not otherwise noted, all connectors are of female type.